PRODUCT SPECIFICATION

5.0” TN TFT LCD MODULE
MODEL: YDP LCD TN 12 500 R 001 Ver:1.9

ROHS

< ◇> Preliminary Specification

< ◆> Finally Specification

CUSTOMER’S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY
TFT W.Q.H 20230719	TFT S.G.H 20230719	TFT 周福云 20230719	TFT L.Q 20230719

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2014.09.05		Initial Release	
1.1	2015.02.09	HXM	Modify Backlight Characteristics Modify Outline Drawing	P6 P27
1.2	2015.03.07	DS	Modify Weight Modify DC Characteristics	P4 P5
1.3	2016.05.26	ZFY	Add IC	
1.4	2017.02.07	ZFY	Add LED working life	P5
1.5	2018.04.26	ZFY	Modify Luminance on TFT Modify many details Modify Outline Drawing	P8 P24/P25 P27
1.6	2019.01.22	ZDT	Add IC Modify Backlight Characteristics Modify AC Characteristics	P4 P6 P13-P15
1.7	2020.01.08	ZDT	Modify VGH/VGL/VCOM Modify Criteria Modify Outline Drawing	P5 P22 P27
1.8	2022.08.12	LQ	Modify CIE and Angle Value Modify Outline Drawing(D)	P7 P27
1.9	2023.07.19	LQ	Modify Module Parameter Modify Optical Characteristics Modify AC Characteristics Modify detail of Reliability Specification Modify Outline Drawing(G)	P4 P7 P13-15 P22 P27

Table of Contents

1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver Ics and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	5.0"	
LCD type	TN TFT	
Display Mode	Normally White/Transmissive	
Resolution	800 RGB x 480	Pixels
View Direction	12 O'clock	Best Image
Gray Scale Inversion Direction	6 O'clock	
Module Outline	120.7 (H) x 75.8(V) x 2.8(T) (Note1)	mm
Active Area	108 (H) x 64.8(V)	mm
Pixel Size	135 (H) x 135(V)	um
Pixel Arrangement	R.G.B Vertical Stripe	
Polarizer Surface Treatment	Anti-Glare	
Display Colors	16.7M	
Interface	24-bit RGB interface	
Driver IC	ILI6137&ILI5960	-
With or Without Touch Panel	Without	
Operating Temperature	**-20～70**	ºC
Storage Temperature	**-30～80**	ºC
Weight	53	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

GND=0V, Ta=25ºC

Item	Symbol	Min.	Max.	Unit
Supply Voltage	DVDD	-0.3	5.0	V
	AVDD	-0.5	13.5	V
	VGH	-0.3	42	V
	VGL	-20	0.3	V
Storage temperature	T_{STG}	**-30**	**+80**	ºC
Operating temperature	T_{OP}	**-20**	**+70**	ºC

Note 1: If Ta below 50ºC, the maximal humidity is 90%RH, if Ta over 50ºC, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10℃, and the back ground will become darker at high temperature operating.

4. DC Characteristics

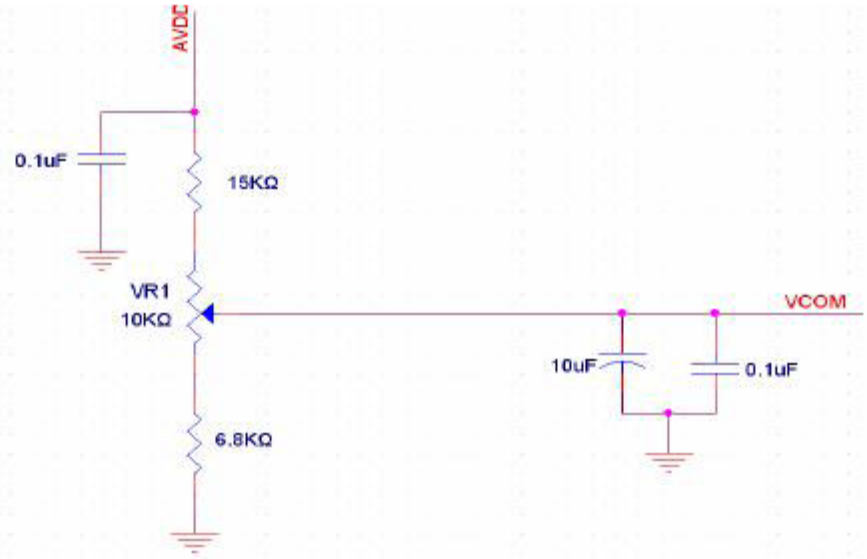
Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	DVDD	3.0	3.3	3.6	V
	AVDD	10.2	10.4	10.6	V
	VGH	14.5	15	15.5	V
	VGL	-8.5	-8	-7.5	V
Digital Supply Current(Black Pattern)	I_{AVDD}	-	25	-	mA
	I_{VGH}	-	470	-	uA
	I_{VGL}	-	-490	-	uA
Input signal voltage	VCOM	3.74	4.24	4.74	V
Logic Low input voltage	V_{IL}	0	-	0.3*DVDD	V
Logic High input voltage	V_{IH}	0.7*DVDD	-	DVDD	V

Note 1: Be sure to apply DVDD and VGL to the LCD first, and then apply VGH.

Note 2: DVDD setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D,L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

Note 4: Typical VCOM is only a reference value. It must be optimized according to each LCM. Please use VR and base on below application circuit.



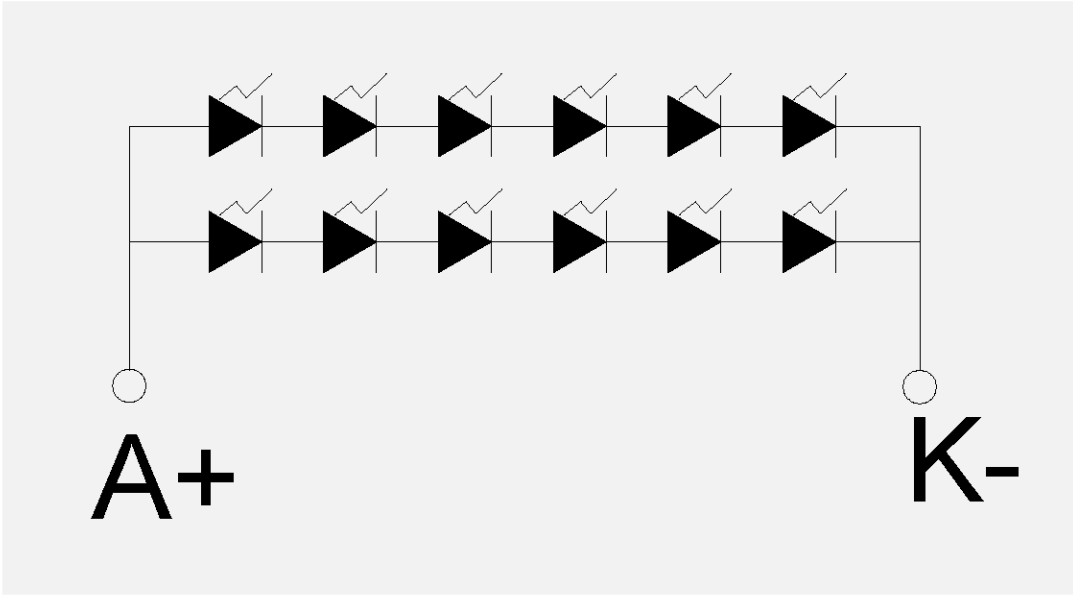
5. Backlight Characteristics

5.1. Backlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Backlight Voltage	V_{LED}	Ta=25 ºC, I_F=20mA/LED	17	**19.2**	22	V
Backlight Current	I_{LED}	Ta=25 ºC, V_F=3.2V/LED	-	**40**	-	mA
Power dissipation	P_D		-	**768**	-	mW
Uniformity	Avg		70	75	-	%
LED working life(25℃)	-		20000	30000	-	Hrs
Drive method	Constant current					
LED Configuration	12 White LEDs(6 LEDs in one string and 2 groups in parallel)					

Note1：LED life time defined as follows: The final brightness is at 50% of original brightness.
The environmental conducted under ambient air flow, at Ta=25±2 ºC,60%RH±5%, I_F=20mA/LED.

5.2. Backlighting circuit



6. Optical Characteristics

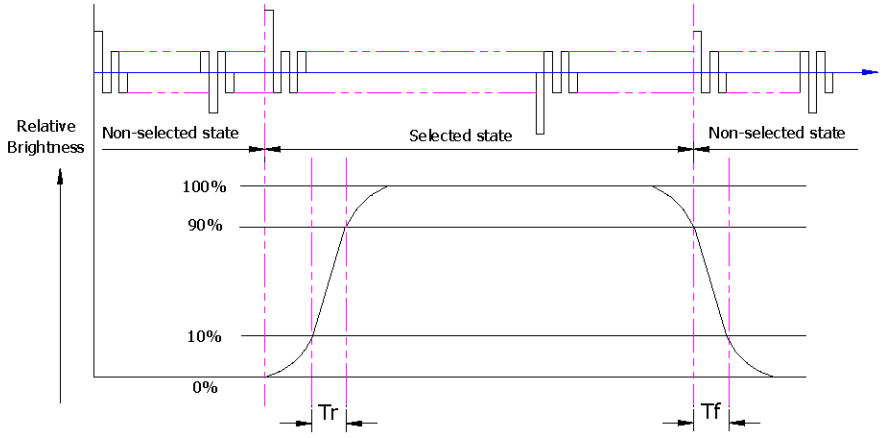
6.1. Optical Characteristics

Ta=25ºC, DVDD=3.3V

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Luminance on TFT(I_f =20mA/LED)		Lv	Normally viewing angle θx = φy =0º	**260**	**330**	-	cd/m²
	Contrast ratio(See 6.3)		CR		400	500	-	
	Response time (See 6.2)		TR+TF		-	25	50	ms
	Chromaticity Transmissive (See 6.5)	Red	XR		**0.520**	**0.570**	**0.620**	
			YR		**0.279**	**0.329**	**0.379**	
		Green	XG		**0.286**	**0.336**	**0.386**	
			YG		**0.558**	**0.608**	**0.658**	
		Blue	XB		**0.099**	**0.149**	**0.199**	
			YB		**0.062**	**0.112**	**0.162**	
		White	XW		**0.269**	**0.319**	**0.369**	
			YW		**0.298**	**0.348**	**0.398**	
	Viewing Angle (See 6.4)	Horizontal	θx+	Center CR≥10	**60**	**70**	-	Deg.
			θx-		**60**	**70**	-	
		Vertical	φy+		**40**	**50**	-	
			φy-		**60**	**70**	-	
	NTSC Ratio(Gamut)				**-**	**49**	-	%

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

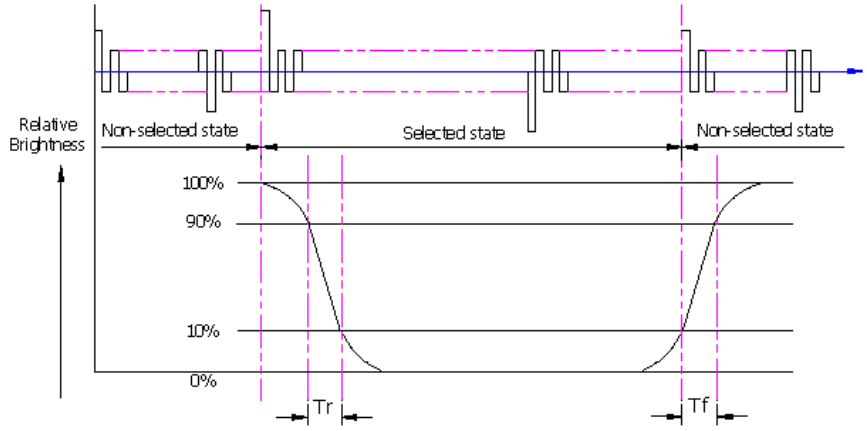


Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%

Note: Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change form non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

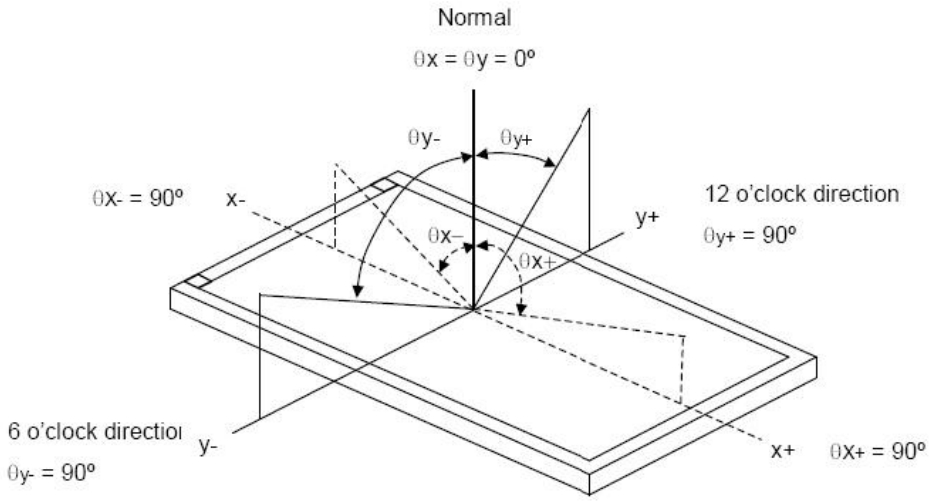
6.3. Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



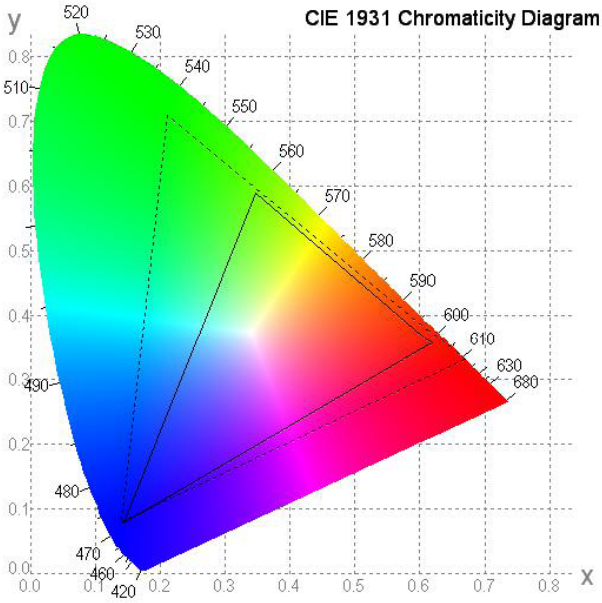
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.6. Definition of Surface Luminance, Uniformity and Transmittance

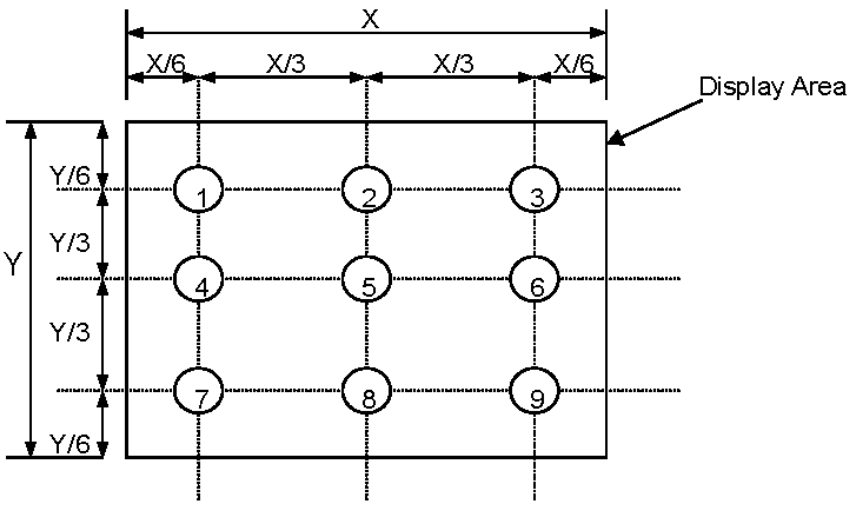
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1. Surface Luminance: L_V = average (L_{P1}:L_{P9})

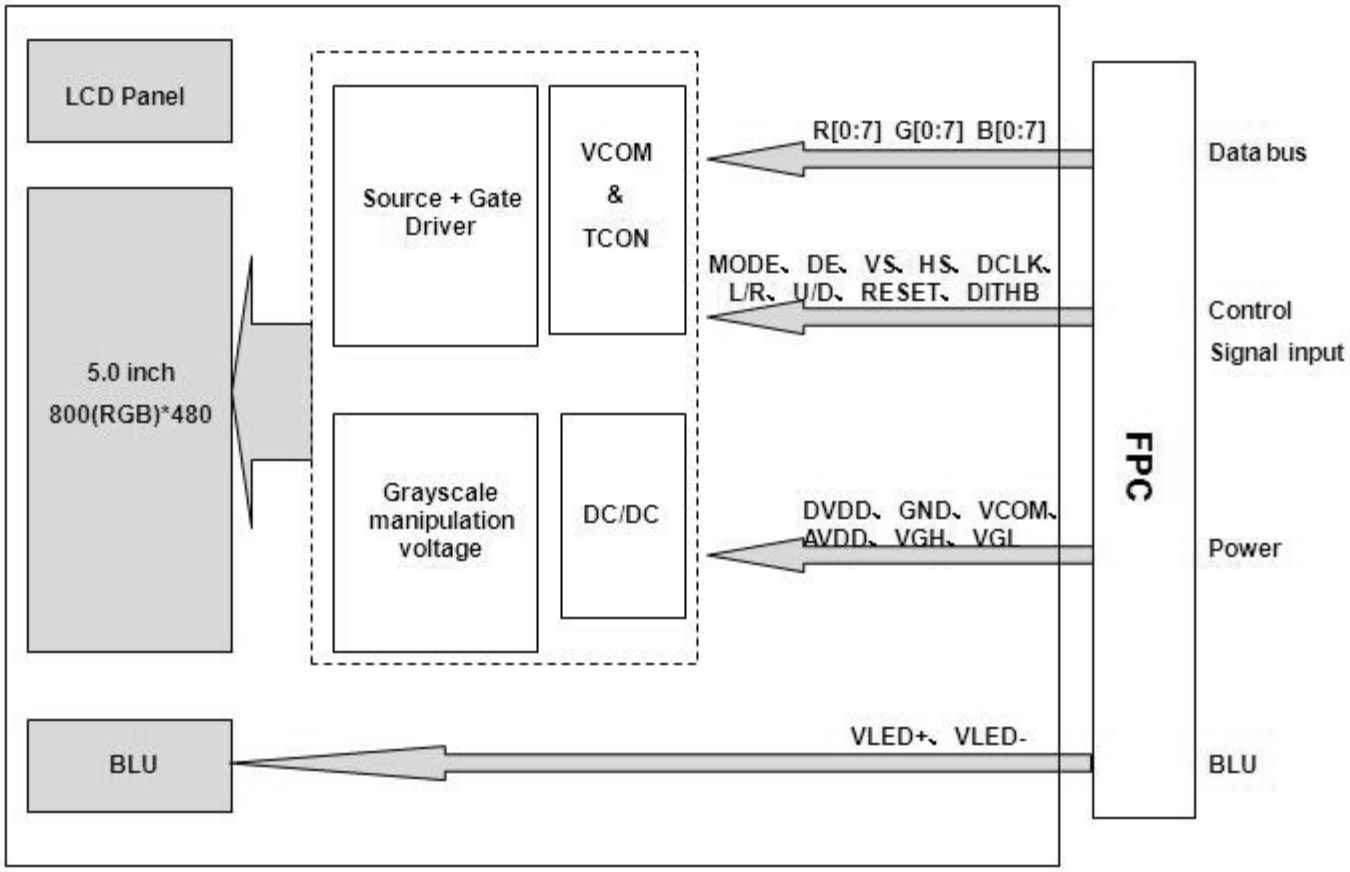
6.6.2. Uniformity = Minimal (L_{P1}:L_{P9}) / Maximal (L_{P1}:L_{P9}) * 100%

6.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

No.	Symbol	Function	Remark
1	VLED+	LED Input Terminal l(Anode).	
2	VLED+	LED Input Terminal l(Anode).	
3	VLED-	Ground (Cathode).	
4	VLED-	Ground (Cathode).	
5	GND	Ground.	
6	VCOM	Common voltage.	
7	DVDD	Power for Digital Circuit.	
8	MODE	DE/SYNC mode select.	Note1
9	DE	Data Input Enable.	
10	VS	Vertical Sync Input.	
11	HS	Horizontal Sync Input.	
12	B7	Blue data(MSB)	
13	B6	Blue data	
14	B5	Blue data	
15	B4	Blue data	
16	B3	Blue data	
17	B2	Blue data	
18	B1	Blue data	Note 2
19	B0	Blue data(LSB)	Note 2
20	G7	Green data(MSB)	
21	G6	Green data	
22	G5	Green data	
23	G4	Green data	
24	G3	Green data	
25	G2	Green data	
26	G1	Green data	Note 2
27	G0	Green data(LSB)	Note 2
28	R7	Red data(MSB)	
29	R6	Red data	
30	R5	Red data	
31	R4	Red data	
32	R3	Red data	
33	R2	Red data	
34	R1	Red data	Note 2
35	R0	Red data(LSB)	Note 2
36	GND	Ground.	
37	DCLK	Sample clock.	Note 3
38	GND	Ground.	
39	L/R	Left / right selection.	Note 4,5
40	U/D	Up / Down selection.	Note 4,5

41	VGH	Gate ON Voltage.	
42	VGL	Gate OFF Voltage.	
43	AVDD	Power for Analog Circuit.	
44	RESET	Global reset pin.	Note 6
45	NC	No connection.	
46	VCOM	Common voltage.	Note 7
47	DITHB	Dithering function.	
48	GND	Ground.	
49	NC	No connection.	
50	NC	No connection.	

Note 1: DE/SYNC mode select. Normally pull high.
When select DE mode, MODE="1", VS and HS must pull high.
When select SYNC mode, MODE="0", DE must be grounded.

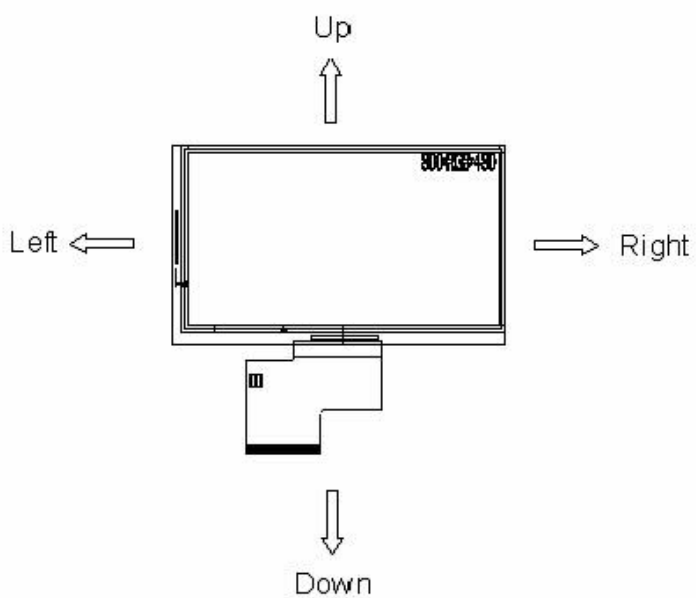
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DVDD	Up to down, left to right
DVDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVDD	DVDD	Down to up, left to right

Note 5: Definition of scanning direction.
Refer to the figure as follow:



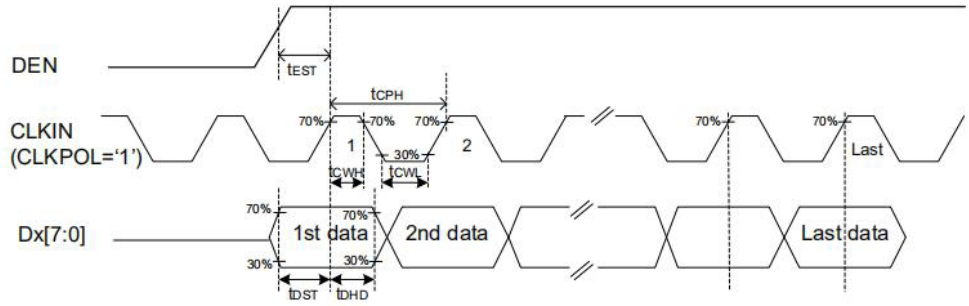
Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with and RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.
When DITHB="1", Disable internal dithering function.
When DITHB="0", Enable internal dithering function.

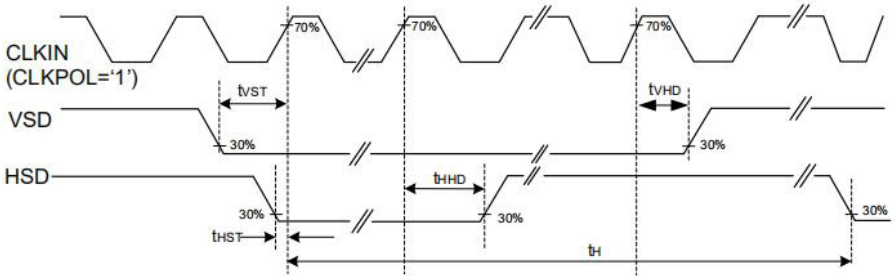
9. AC Characteristics

9.1. AC Timing characteristics

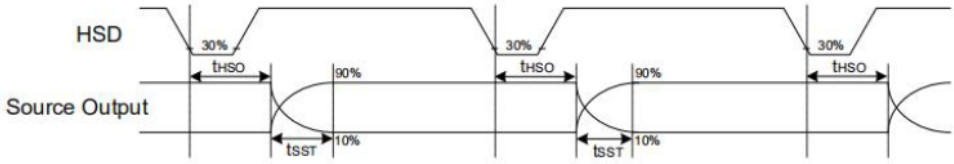
DE Mode (MODE= H)



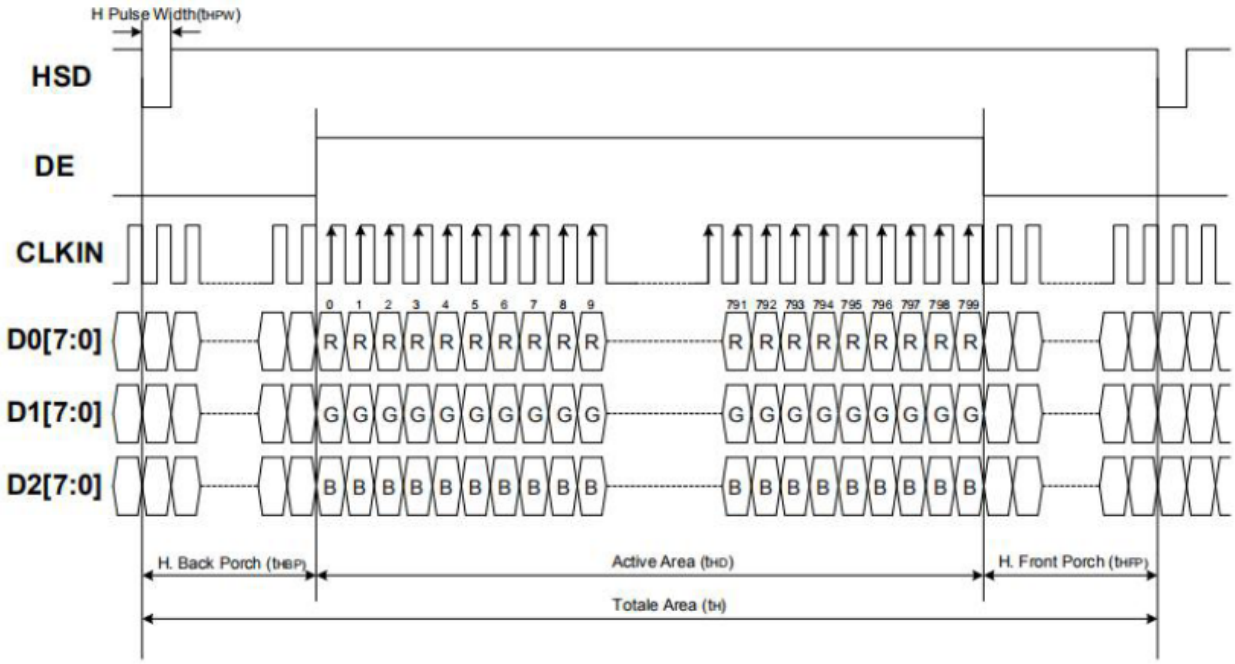
SYNC Mode (MODE= L)



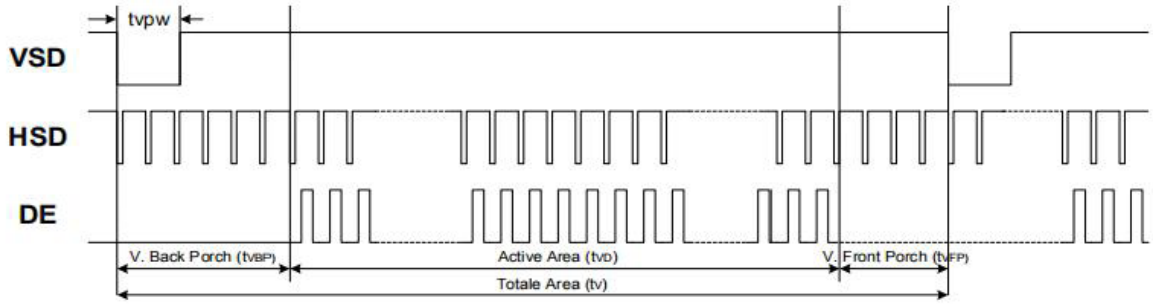
Source Output timing Diagram (Cascade)



9.2. Display Timing characteristics



Horizontal Input Timing						
Parameter		Symbol	Value			Unit
			Min.	Typ.	Max.	
Horizontal display area		t_{HD}	--	800	--	CLKIN
CLKIN frequency		f_{CLK}	--	33.3	50	MHz
1 Horizontal line period		t_H	862	1056	1200	CLKIN
HSD pulse width	Min.	t_{HPW}	--	1	--	CLKIN
	Typ.		--	--	--	CLKIN
	Max.		--	40	--	CLKIN
HSD back porch	SYNC	t_{HBP}	46	46	46	CLKIN
HSD front porch	SYNC	t_{HFP}	16	210	354	CLKIN



Vertical Input Timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	t_{VD}	--	480	--	HSD
VSD period time	t_V	510	525	650	HSD
VSD pulse width	t_{VPW}	1	--	20	HSD
VSD back porch	t_{VBP}	23	23	23	HSD
VSD front porch	t_{VFP}	7	22	147	HSD

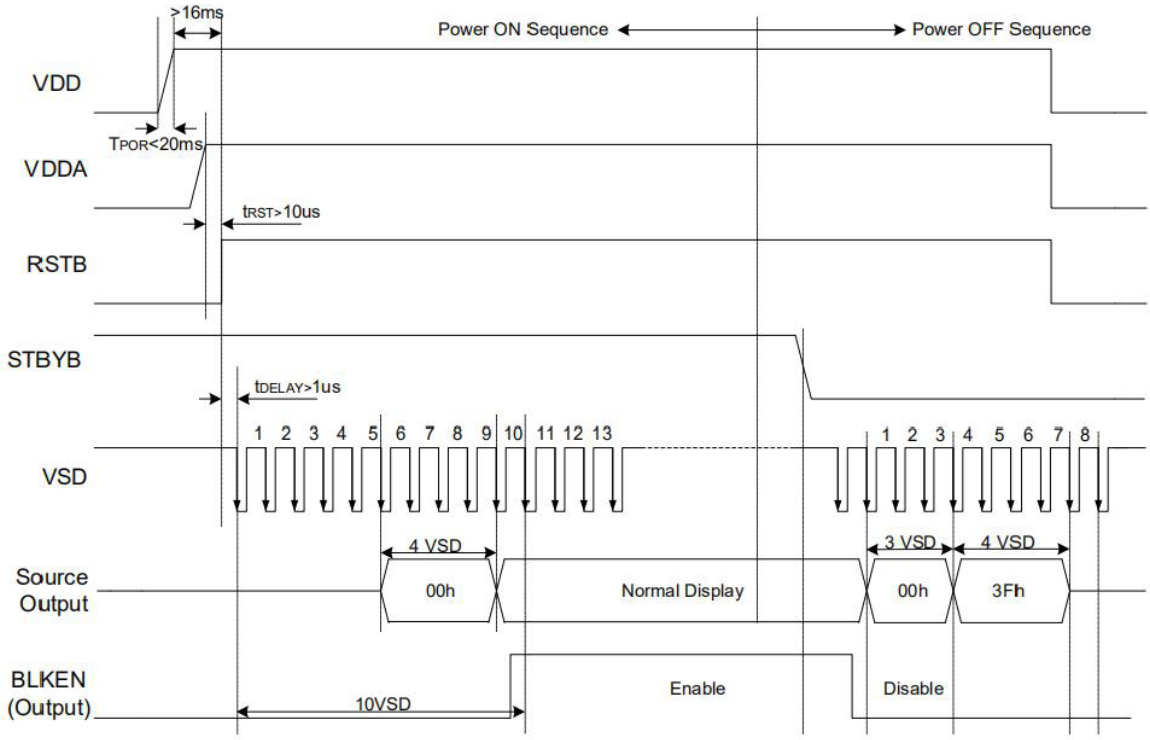
9.3. Power ON/Off Sequence

To prevent the device damage from latch up, the power ON/OFF sequence shown below must be followed.

Power ON: VDD, DGND→ VDDA, AGND → V1 to V14

Power OFF: V1 to V14 → VDDA, AGND→ VDD, DGND

In order to prevent ILI6137A from power ON reset fail, the rising time (t_{POR}) of the digital power supply VDD should be maintained within given specifications. The power ON/OFF timing sequence is illustrated as below:



Note: For preventing abnormal operation, t_{RST} must be longer than 10us during Power ON sequence.

10. Quality Assurance

10.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

10.2.Standard for Quality Test

10.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

10.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

10.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.3.Nonconforming Analysis & Disposition

10.3.1. Nonconforming analysis:

10.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

10.3.2. Disposition of nonconforming:

10.3.2.1. Non-conforming product over PPM level will be replaced.

10.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

10.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

10.4.1. There is any discrepancy in standard of quality assurance.

10.4.2. Additional requirement to be added in product specification.

10.4.3. Any other special problem.

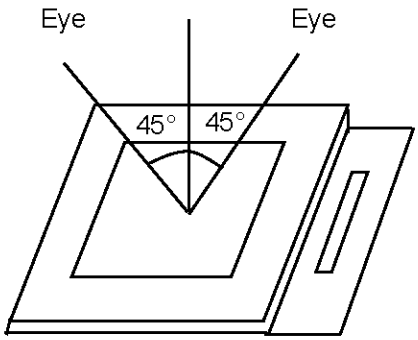
10.5. Standard of the Product Visual Inspection

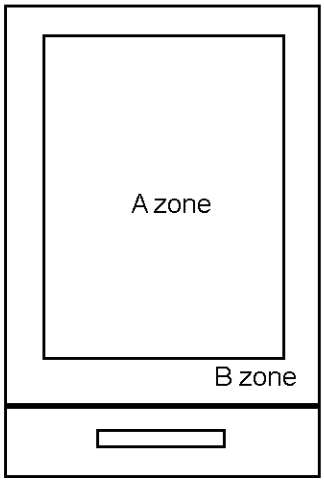
10.5.1. Appearance inspection:

10.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,



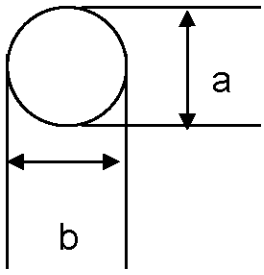


10.5.2. Basic principle:

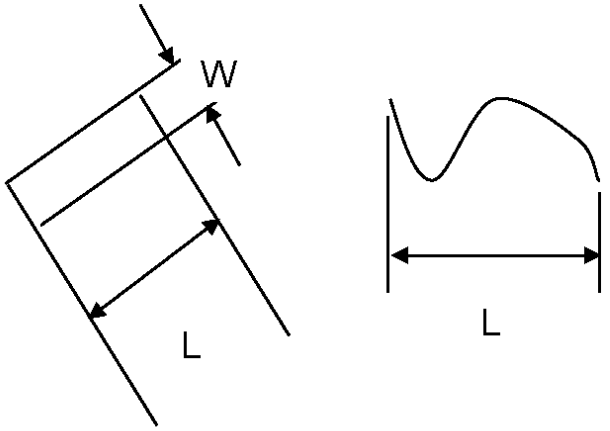
10.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

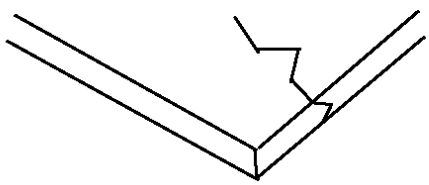
10.5.2.2. New item must be added on time when it is necessary.

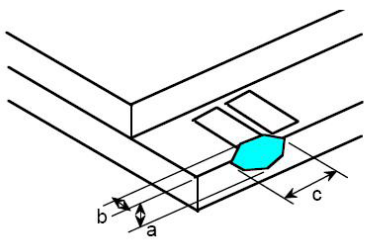
10.6.Inspection Specification

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>01</td><td>Black / White spot
Foreign material (Round type)
Pinholes
Stain
Particles inside cell. (Minor defect)</td><td>


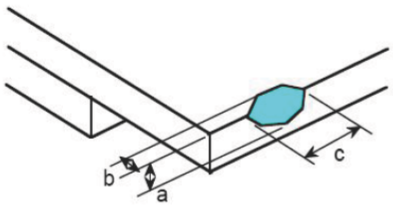
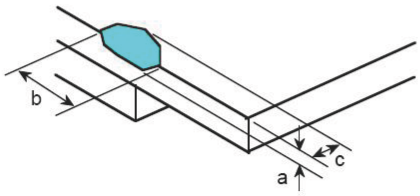
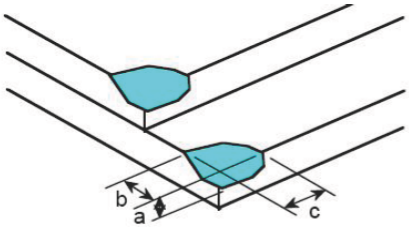
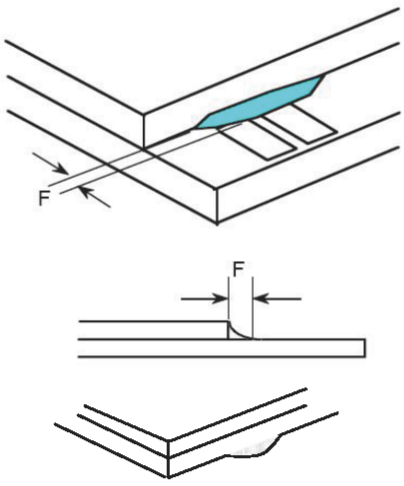
<table>
<tr><th>Area / Size</th><th>Acc. Qty</th></tr>
<tr><td>φ≤0.20</td><td>Ignore</td></tr>
<tr><td>0.20<φ≤0.50</td><td>N≤3</td></tr>
<tr><td>0.50<φ</td><td>0</td></tr>
</table>
φ= (a + b) /2
Distance between 2 defects should more than 5mm apart.
</td></tr>
<tr><td>02</td><td>Electrical Defect (Minor defect)</td><td>
<table>
<tr><td rowspan="2">Bright dot</td><td>Display Area</td><td>Total</td><td rowspan="3">Note1</td></tr>
<tr><td>N≤2</td><td>N≤2</td></tr>
<tr><td>Dark dot</td><td>N≤4</td><td>N≤4</td></tr>
<tr><td>Total dot</td><td>N≤4</td><td>N≤4</td><td></td></tr>
<tr><td>Mura</td><td colspan="2">Not visible through 5% ND filters.</td><td>Note 2</td></tr>
</table>
Remark:
1. Bright dot caused by scratch and foreign object accords to item 1.
</td></tr>
</table>

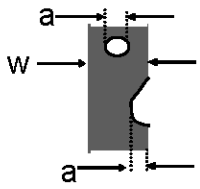
<table>
<tr>
<td>03</td>
<td>Black and White line
Scratch
Foreign material
(Line type)
(Minor defect)</td>
<td>


<table>
<tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr>
<tr><td>/</td><td>W ≦ 0.1</td><td>Ignore</td></tr>
<tr><td>L ≦ 2.5</td><td>0.1 ＜ W ≦ 0.2</td><td>3</td></tr>
<tr><td>L＞2.5</td><td>0.2 ＜ W</td><td>0</td></tr>
<tr><td colspan="2">Total</td><td>3</td></tr>
</table>
Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.
</td>
</tr>
<tr>
<td>04</td>
<td>Glass Crack
(Minor defect)</td>
<td>

Crack is potential to enlarge, any type is not allowed.
</td>
</tr>
</table>

<table>
<tr>
<td>05</td>
<td>Glass Chipping Pad Area:
(Minor defect)


</td>
<td>
<table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>3</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table>
</td>
</tr>
</table>

<table>
<tr>
<td>06</td>
<td>Glass Chipping Rear of Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>07</td>
<td>Glass Chipping Except Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>08</td>
<td>Glass Corner Chipping:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c < 3.0, b< 3.0</td><td>Ignore</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>09</td>
<td>Glass Burr:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length</th><th>Acc. Qty</th></tr>
<tr><td>F < 1.0</td><td>Ignore</td></tr>
</table>
Glass burr don’t affect assemble and module dimension.</td>
</tr>
</table>

<table>
<tr><td>10</td><td>FPC Defect:
(Minor defect)
 
</td><td>10.1 Dent, pinhole width a<w/3.
(w: circuitry width.)
10.2 Open circuit is unacceptable.
10.3 No oxidation, contamination and distortion.</td></tr>
<tr><td>11</td><td>Bubble on Polarizer
(Minor defect)</td><td><table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>φ≤0.30</td><td>Ignore</td></tr><tr><td>0.30 <φ≤0.50</td><td>N≤2</td></tr><tr><td>0.50 < φ</td><td>N=0</td></tr></table></td></tr>
<tr><td>12</td><td>Dent on Polarizer
(Minor defect)</td><td><table><tr><th>Diameter</th><th>Acc. Qty</th></tr><tr><td>φ≤0.25</td><td>Ignore</td></tr><tr><td>0.25 <φ≤0.50</td><td>N≤4</td></tr><tr><td>0.50 < φ</td><td>None</td></tr></table></td></tr>
<tr><td>13</td><td>Bezel</td><td>13.1 No rust, distortion on the Bezel.
13.2 No visible fingerprints, stains or other contamination.</td></tr>
<tr><td>14</td><td>Touch Panel</td><td>D: Diameter W: width L: length
14.1 Spot: D<0.25 is acceptable
0.25≤D≤0.4
2dots are acceptable and the distance between defects should more than 10 mm.
D>0.4 is unacceptable
14.2 Dent: D>0.40 is unacceptable
14.3 Scratch: W≤0.03, L≤10 is acceptable,
0.03<W≤0.10, L≤10 is acceptable
Distance between 2 defects should more than 10 mm.
W>0.10 is unacceptable.</td></tr>
<tr><td>15</td><td>PCB</td><td>15.1 No distortion or contamination on PCB terminals.
15.2 All components on PCB must same as documented on the BOM/component layout.
15.3 Follow IPC-A-600F.</td></tr>
<tr><td>16</td><td>Soldering</td><td>Follow IPC-A-610C standard</td></tr>
</table>

17	Electrical Defect (Major defect)	The below defects must be rejected. 17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

10.7.Classification of Defects

10.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

10.7.2. Two minor defects are equal to one major in lot sampling inspection.

10.8.Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

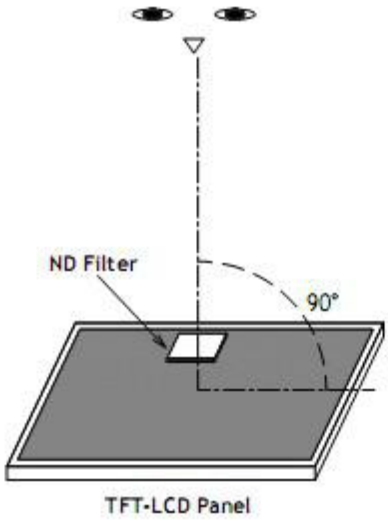
10.9.Packing

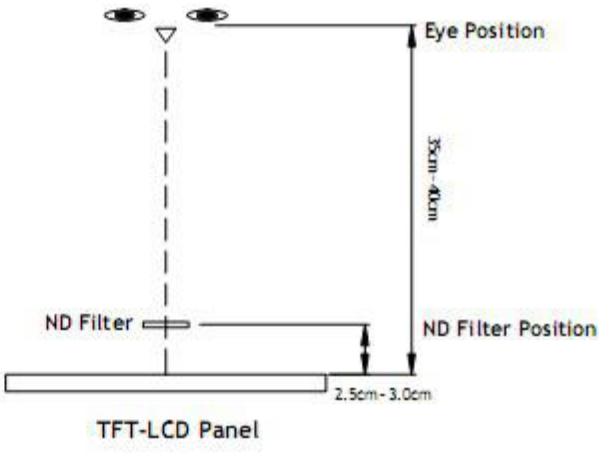
10.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

10.9.2. Modules inside package box should have compliant mark.

10.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.





Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

11. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	**70℃, 96Hrs**	2	GB/T2423.2 -2008
2	Low Temperature Operating	**-20℃, 96Hrs**	2	GB/T2423.1 -2008
3	High Humidity Storage	**50℃, 90%RH, 96Hrs**	2	GB/T2423.3 -2006
4	High Temperature Storage	**80℃, 96Hrs**	2	GB/T2423.2 -2008
5	Low Temperature Storage	**-30℃, 96Hrs**	2	GB/T2423.1 -2008
6	Thermal Cycling Test Storage	**-20℃, 60min~70℃, 60min, 20 cycles.**	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	-	GB/T5170.14 -2009
8	Electrical Static Discharge	Air:±8KV 150pF/330Ω 5 times Contact:±4KV 150pF/330Ω 5 times	2	GB/T17626.2 -2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	-	GB/T2423.8 -1995

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

12. Precautions and Warranty

12.1.Safety

12.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

12.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2.Handling

12.2.1. Reverse and use within ratings in order to keep performance and prevent damage.

12.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3.Storage

12.3.1. Do not store the LCD module beyond the specified temperature ranges.

12.3.2. Strong light exposure causes degradation of polarizer and color filter.

12.4.Metal Pin (Apply to Products with Metal Pins)

12.4.1. Pins of LCD and Backlight

12.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

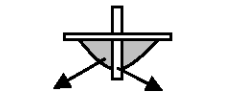
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

12.4.1.3. Solder Wetting



Recommended

Not Recommended

12.4.2. Pins of EL

12.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

12.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270～290℃

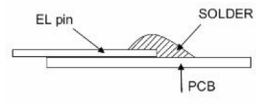
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

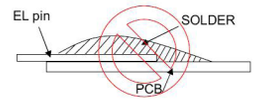
12.4.2.4. No horizontal press on the EL leads during soldering.

12.4.2.5. 180° bend EL leads three times is not allowed.

12.4.2.6. Solder Wetting

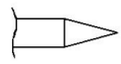


Recommended

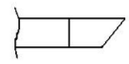


Not Recommended

12.4.2.7. The type of the solder iron:

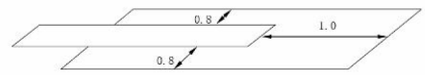


Recommended



Not Recommended

12.4.2.8. Solder Pad



12.5. Operation

12.5.1. Do not drive LCD with DC voltage

12.5.2. Response time will increase below lower temperature

12.5.3. Display may change color with different temperature

12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

12.5.5. Do not connect or disconnect the LCM to or from the system when power is on.

12.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.

12.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

12.5.8. Do not display the fixed pattern for long time（we suggest the time not longer than one hour）because it will develop image sticking due to the TFT structure.

12.6. Static Electricity

12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

12.6.2. The normal static prevention measures should be observed for work clothes and benches.

12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7. Limited Warranty

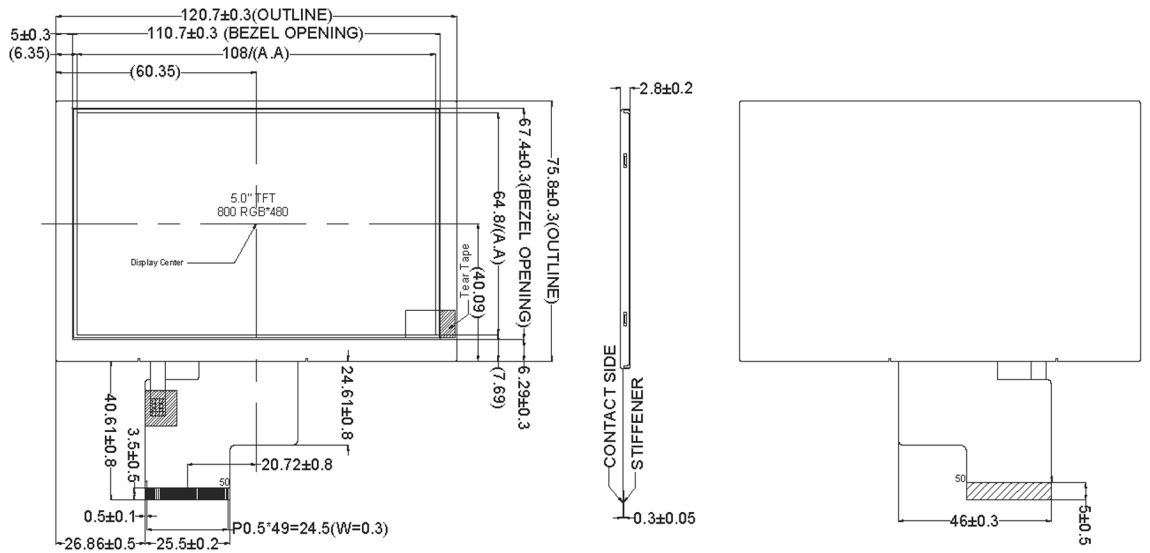
12.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.

12.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.

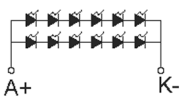
12.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Packaging

TBD



BACKLIGHT CIRCUIT



NO.	PIN NAME
1	VLED+
2	VLED+
3	VLED-
4	VLED-
5	GND
6	VCOM
7	DVDD
8	MODE
9	DE
10	VS
11	HS
12	B7
13	B6
14	B5
15	B4
16	B3
17	B2
18	B1
19	B0
20	G7
21	G6
22	G5
23	G4
24	G3
25	G2
26	G1
27	G0
28	R7
29	R6
30	R5
31	R4
32	R3
33	R2
34	R1
35	R0
36	GND
37	DCLK
38	GND
39	L/R
40	U/D
41	VGH
42	VGL
43	AVDD
44	RESET
45	NC
46	VCOM
47	DITHB
48	GND
49	NC
50	NC

NOTES:
1.Display size:5.0"TFT
2.Viewing direction:12 O'CLOCK
3.Gary Scale inversion direction:6 O'CLOCK
4.Display mode:Transmissive/Normal white/Anti-glare
5.Operation temperature:-20°C~+70°C
6.Storage temperature:-30°C~+80°C
7.Power supply voltage:3.3V
8.Driver IC:ILI6137&ILI5960
9.Backlight :White(12 LED)/17-22V/40mA
10.Brighness: 330(TYP)cd/m2
11.ROHS must be complied
* Unspecification tolerance are ±0.2mm
* The dimension with mark brackets "()" just for reference

Compliance: RohS III(2015/863/EU)

Tolerances:			Date	Name
			02/25	dr
Modifications	Date	Name		

YDP LCD TN 12 500 R 001

knitter-switch

30 56 86

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