PRODUCT SPECIFICATION

2.13” TFT LCD MODULE
MODEL: YDP LCD MN 213 M

ROHS

< ◇> Preliminary Specification

< ◆> Finally Specification

CUSTOMER’S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY
-	TFT S. G. H 20240314	TFT 周福云 20240314	TFT L. L 20240314

knitter-switch

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2022.06.10	ZYJ	Initial Release	
1.1	2022.08.15	ZYJ	Modify Module Parameter Modify Current Consumption Modify Reset Timing Modify Outline Drawing(B)	P4 P5 P12 P23
1.2	2024.03.14	LL	Modify display mode Modify Outline Drawing(B)	P4 P23

Table of Contents

1. General Description

The specification is a eflective type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs .

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	2.13”	
Display Mode	Reflective /Normally White	
Resolution	122 x 250 DOTS	Pixels
View Direction	Full view	Best Image
Module Outline	27.068(H) x 56.2(V) x 0.93(T) (Note1)	mm
Active Area	23.668 (H) x 48.5 (V)	mm
Pixel Size	194 x 194	um
Pixel Arrangement	Mono Stripe	
Polarizer Surface Treatment	Anti-glare	
Interface	8-bit MCU Interface	
With or without touch panel	Without	
Driver IC	ST7305	-
Operating Temperature	**-20～70**	ºC
Storage Temperature	**-30～80**	ºC
Weight	4	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

VSS=0V, Ta=25ºC

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.3	4.0	V
Storage temperature	T_{STG}	**-30**	**+80**	ºC
Operating temperature	T_{OP}	**-20**	**+70**	ºC

Note 1: If Ta below 50ºC, the maximal humidity is 90%RH, if Ta over 50ºC, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10℃, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	2.55	2.8	3.6	V
Logic Low input voltage	V_{IL}	VSS	-	0.3*VDD	V
Logic High input voltage	V_{IH}	0.7*VDD	-	VDD	V
Logic Low output voltage	V_{OL}	VSS	-	0.2*VDD	V
Logic High output voltage	V_{OH}	0.8*VDD	-	VDD	V
Current Consumption All Black	I_{VDD}	-	6	-	μA

5. Optical Characteristics

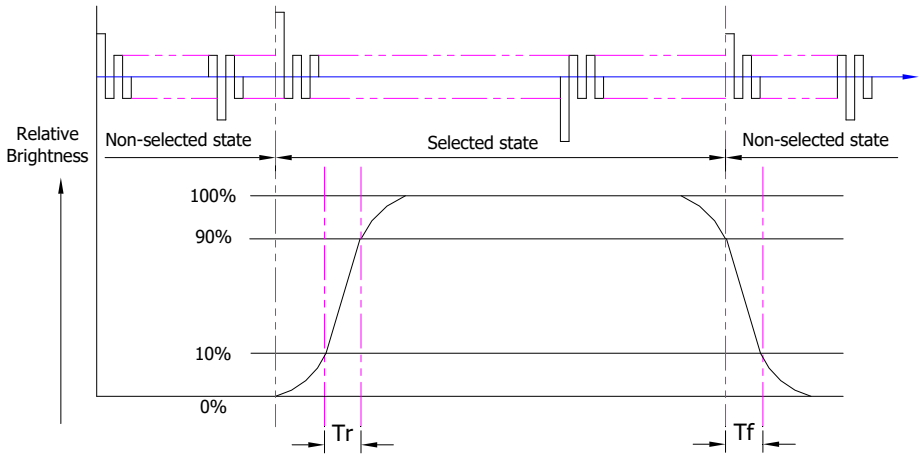
5.1. Optical Characteristics

Ta=25ºC, VDD=2.8V

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
(Reflective Mode)	Luminance on TFT(I_f =TBDmA/LED)		Lv	Normally viewing angle θX = φY =0º	-	**TBD**	-	cd/m²
	Contrast ratio(See 5.3)		CR		-	(14)	-	
	Response time (See 5.2)		TR+TF		-	(30)	(40)	ms
	Chromaticity Reflective (See 5.5)	Red	XR		-	**TBD**	-	
			YR		-	**TBD**	-	
		Green	XG		-	**TBD**	-	
			YG		-	**TBD**	-	
		Blue	XB		-	**TBD**	-	
			YB		-	**TBD**	-	
		White	XW		-	**TBD**	-	
			YW		-	**TBD**	-	
	Viewing Angle (See 5.4)	Horizontal	θX+	Center CR>5	-	**60**	-	Deg.
			θX-		-	**60**	-	
		Vertical	φY+		-	**45**	-	
			φY-		-	**45**	-	
	NTSC Ratio(Gamut)				-	TBD	-	%

5.2. Definition of Response Time

5.2.1. Normally Black Type (Negative)



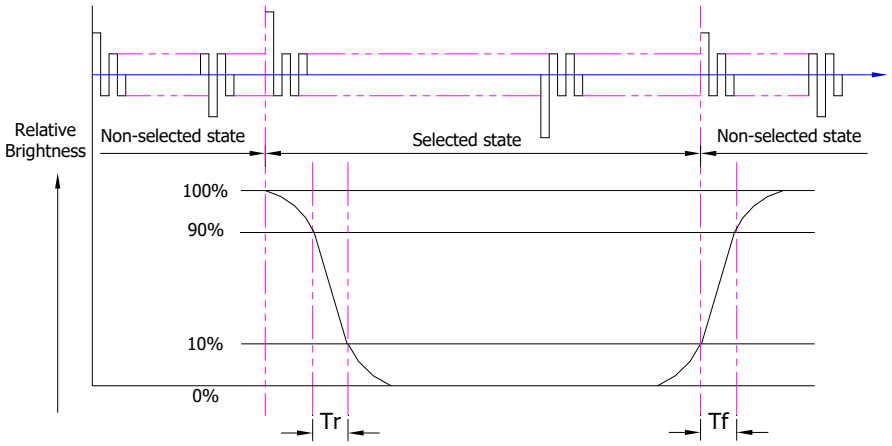
Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to

non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

5.2.2. Normally White Type (Positive)



Tr is the time it takes to change form non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

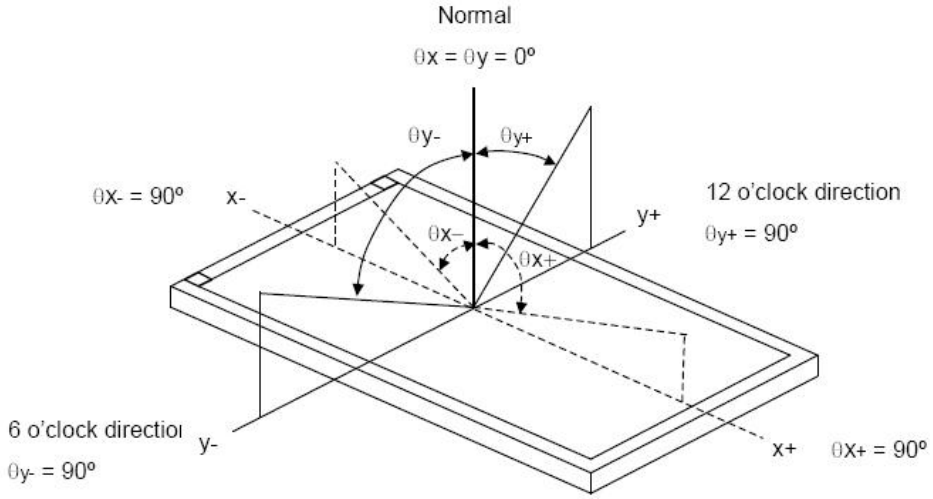
5.3. Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

5.4. Definition of Viewing Angles



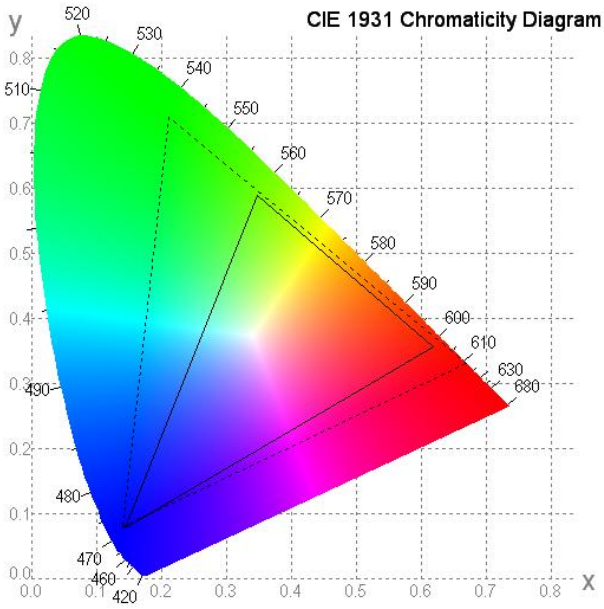
Measuring machine: LCD-5100 or EQUI

5.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



5.6. Definition of Surface Luminance, Uniformity and Transmittance

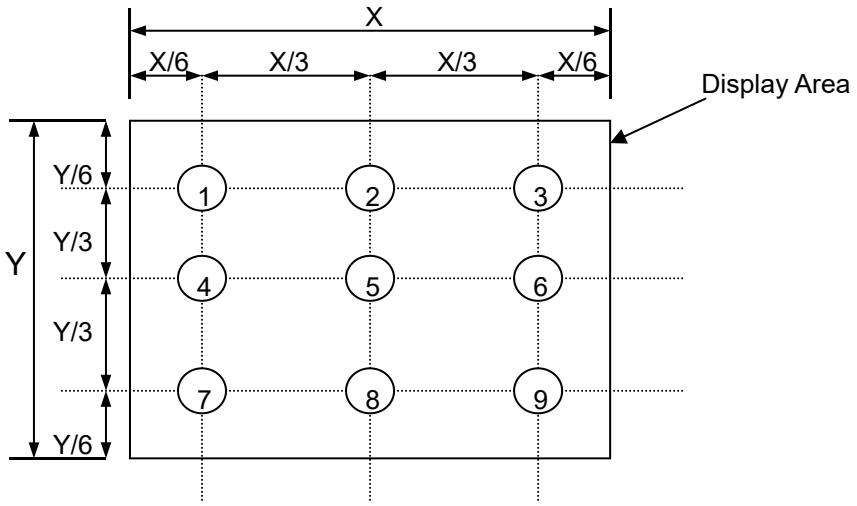
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

5.6.1. Surface Luminance: L_V = average (L_{P1}:L_{P9})

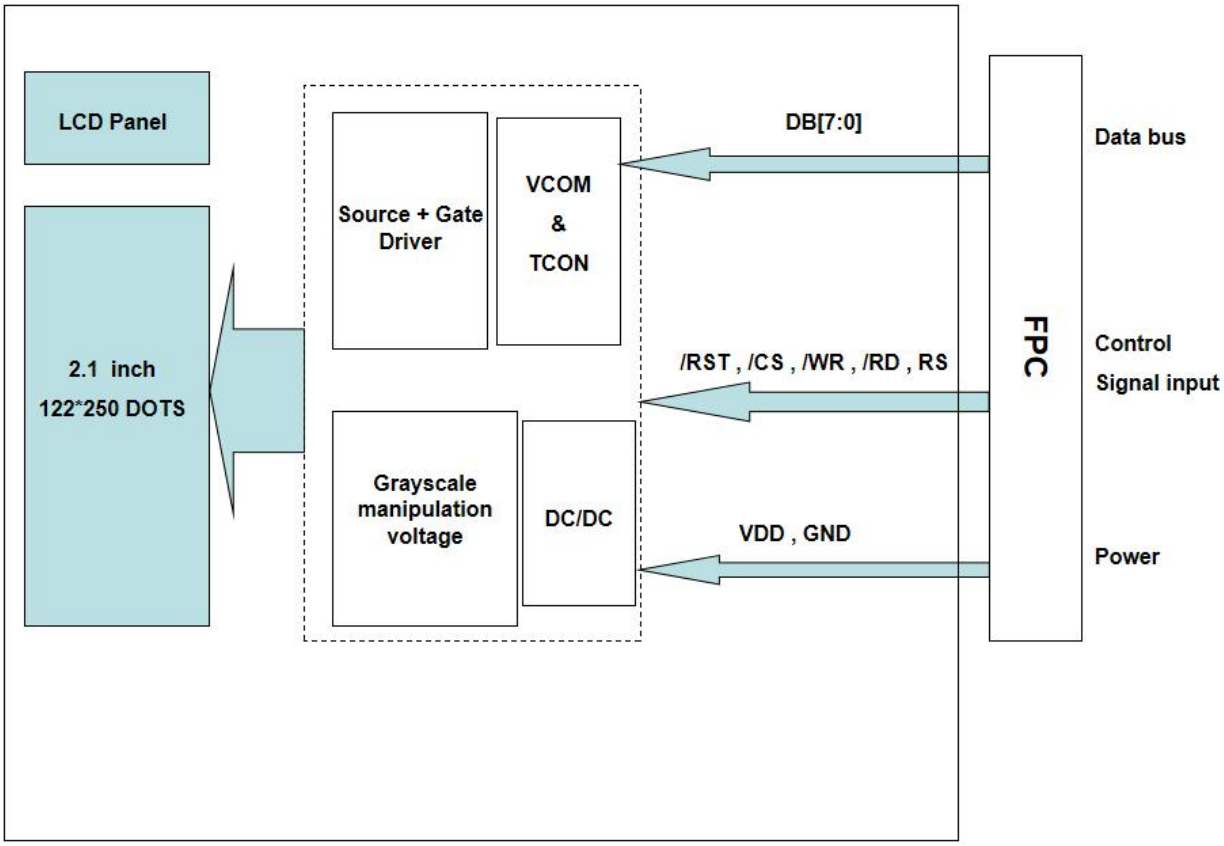
5.6.2. Uniformity = Minimal (L_{P1}:L_{P9}) / Maximal (L_{P1}:L_{P9}) * 100%

5.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



6. Block Diagram and Power Supply

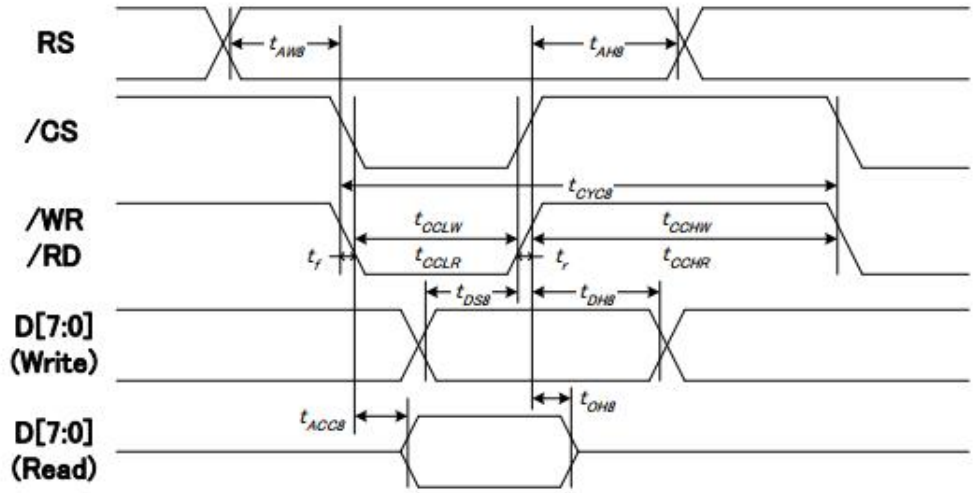


7. Interface Pins Definition

No.	Symbol	Function
1	/RST	Reset input pin
2	/RD	Read enable pin
3	/WR	Write enable pin
4	/CS	Chip select input pin
5	RS	Data/Command identification pin
6	DB0	Data bus
7	DB1	Data bus
8	DB2	Data bus
9	DB3	Data bus
10	DB4	Data bus
11	DB5	Data bus
12	DB6	Data bus
13	DB7	Data bus
14	VDD	Power supply
15	VSS	Ground
16	NC	No connection
17	NC	No connection
18	NC	No connection
19	NC	No connection
20	NC	No connection
21	NC	No connection
22	NC	No connection
23	NC	No connection
24	NC	No connection

8. AC Characteristics

8.1. MCU Interface Characteristics



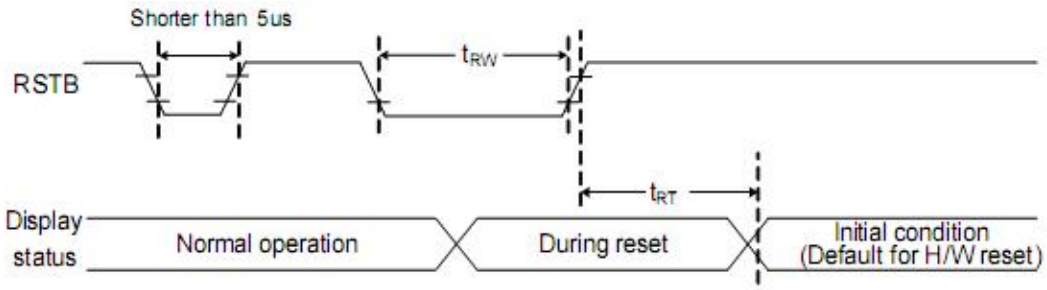
VDD = 2.8V, Ta = 25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	RS	tAW8		20	—	ns
Address hold time		tAH8		0	—	
System cycle time (WRITE)	/WR	tCYC8		160	—	
/WR L pulse width (WRITE)		tCCLW		70	—	
/WR H pulse width (WRITE)		tCCHW		70	—	
System cycle time (READ)	/RD	tCYC8		400	—	
/RD L pulse width (READ)		tCCLR		180		
/RD H pulse width (READ)		tCCHR		180		
WRITE Data setup time	D[7:0]	tDS8		15	—	
WRITE Data hold time		tDH8		15	—	
READ access time		tACC8	CL = 30 pF	—	100	
READ Output disable time		tOH8	CL = 30 pF	10	110	

Note:

1. The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. When the system cycle time is extremely fast, (tr + tf) ≦ (tCYC8 – tCCLW – tCCHW) for (tr + tf) ≦ (tCYC8 – tCCLR – tCCHR) are specified.
2. All timing is specified using 20% and 80% of VDD as the reference.

8.2. Reset Timing



VDD = 2.8V, Ta=25˚C

Item	Symbol	Condition	Min.	Max.	Unit
Reset "L" pulse width	tRW		1	—	ms
Reset cancel	tRT	Note1, 5 (sleep-in mode)	—	5	ms
		Note1, 6, 7 (sleep-out mode)	—	120	ms

9. Quality Assurance

9.1. Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

9.2. Standard for Quality Test

9.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

9.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

9.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

9.3. Nonconforming Analysis & Disposition

9.3.1. Nonconforming analysis:

9.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

9.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

9.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

9.3.2. Disposition of nonconforming:

9.3.2.1. Non-conforming product over PPM level will be replaced.

9.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

9.4. Agreement Items

Shall negotiate with customer if the following situation occurs:

9.4.1. There is any discrepancy in standard of quality assurance.

9.4.2. Additional requirement to be added in product specification.

9.4.3. Any other special problem.

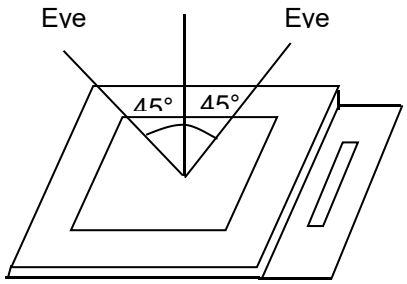
9.5. Standard of the Product Visual Inspection

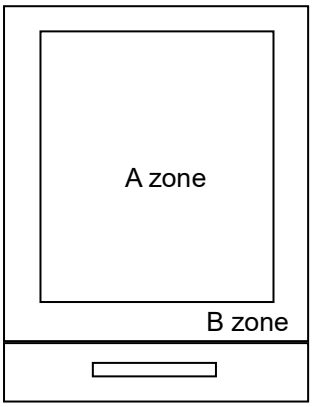
9.5.1. Appearance inspection:

9.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

9.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

9.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,



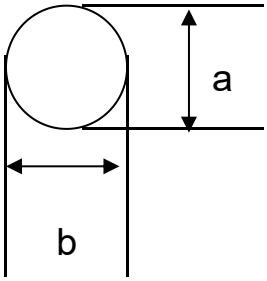


9.5.2. Basic principle:

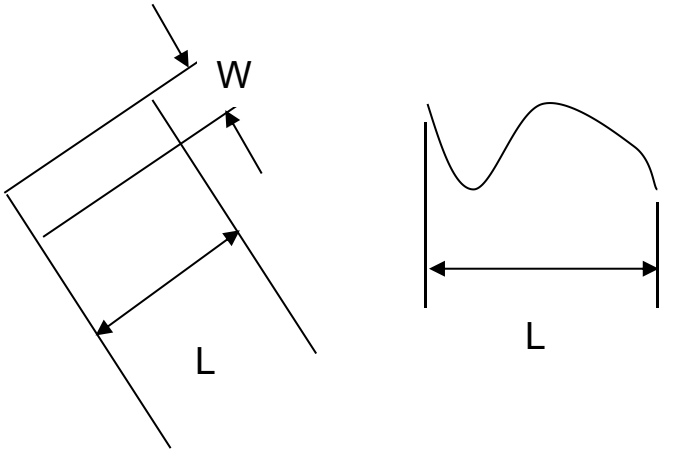
9.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

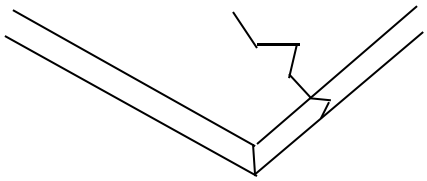
9.5.2.2. New item must be added on time when it is necessary.

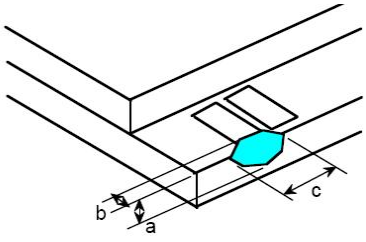
9.6. Inspection Specification

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>01</td><td>Black / White spot
Foreign material
(Round type)
Pinholes
Stain
Particles inside cell.
(Minor defect)</td><td>


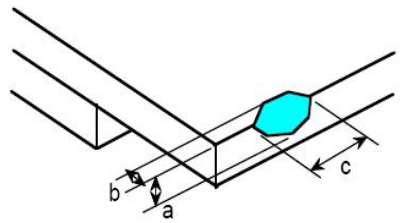
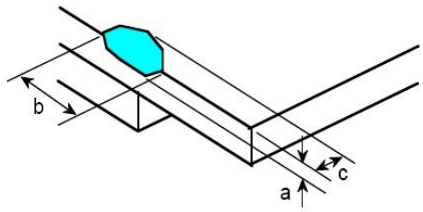
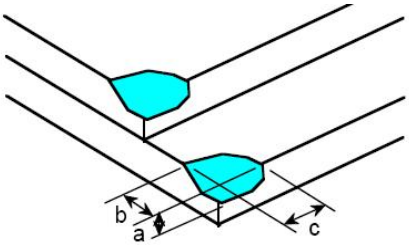
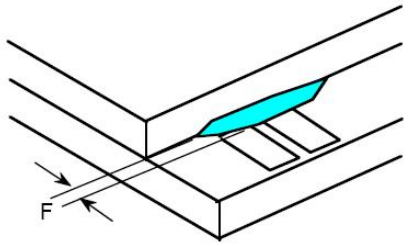
<table>
<tr><th>Area / Size</th><th>Acc. Qty</th></tr>
<tr><td>φ≤0.10</td><td>Ignore</td></tr>
<tr><td>0.10<φ≤0.15</td><td>2</td></tr>
<tr><td>0.15<φ≤0.25</td><td>1</td></tr>
<tr><td>0.25<φ</td><td>0</td></tr>
<tr><td>Total</td><td>2 no include
φ≤ 0.10</td></tr>
</table>
φ= (a + b) /2
Distance between 2 defects should more than 3mm apart.
</td></tr>
<tr><td>02</td><td>Electrical Defect
(Minor defect)</td><td>
<table>
<tr><td></td><td>Display Area</td><td>Total</td><td rowspan="4">Note1</td></tr>
<tr><td>Bright dot</td><td>0</td><td>0</td></tr>
<tr><td>Dark dot</td><td>N≤2</td><td>N≤2</td></tr>
<tr><td>Total dot</td><td>N≤2</td><td>N≤2</td></tr>
<tr><td>Mura</td><td colspan="2">Not visible through 5% ND filters.</td><td>Note2</td></tr>
</table>
Remark:
1. Bright dot caused by scratch and foreign object accords to item 1.
</td></tr>
</table>

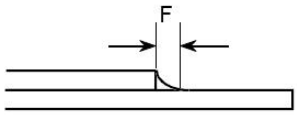
<table>
<tr>
<td>03</td>
<td>Black and White line
Scratch
Foreign material
(Line type)
(Minor defect)</td>
<td>


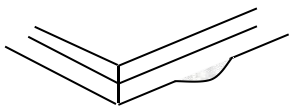
<table>
<tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr>
<tr><td>/</td><td>W ≦ 0.03</td><td>Ignore</td></tr>
<tr><td>L ≦ 2.5</td><td>0.03 ＜ W ≦ 0.05</td><td>3</td></tr>
<tr><td>L ≦ 2.5</td><td>0.05 ＜ W ≦ 0.10</td><td>2</td></tr>
<tr><td>/</td><td>0.1 ＜ W</td><td></td></tr>
<tr><td colspan="2">Total</td><td>3</td></tr>
</table>
Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.
</td>
</tr>
<tr>
<td>04</td>
<td>Glass Crack
(Minor defect)</td>
<td>

Crack is potential to enlarge, any type is not allowed.
</td>
</tr>
</table>

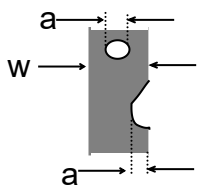
<table>
<tr>
<td>05</td>
<td>Glass Chipping Pad Area:
(Minor defect)


</td>
<td>
<table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>3</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table>
</td>
</tr>
</table>

<table>
<tr>
<td>06</td>
<td>Glass Chipping Rear of Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>07</td>
<td>Glass Chipping Except Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>08</td>
<td>Glass Corner Chipping:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c < 3.0, b< 3.0</td><td>Ignore</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>09</td>
<td>Glass Burr:
(Minor defect)
 

  </td>
<td><table>
<tr><th>Length</th><th>Acc. Qty</th></tr>
<tr><td>F < 1.0</td><td>Ignore</td></tr>
</table>
Glass burr don’t affect assemble and module dimension.</td>
</tr>
</table>

<table>
<tr><td>10</td><td>FPC Defect:
(Minor defect)
  </td><td>10.1 Dent, pinhole width a<w/3.
(w: circuitry width.)
10.2 Open circuit is unacceptable.
10.3 No oxidation, contamination and distortion.</td></tr>
<tr><td>11</td><td>Bubble on Polarizer
(Minor defect)</td><td><table><tr><td>Diameter</td><td>Acc. Qty</td></tr><tr><td>φ≤0.20</td><td>Ignore</td></tr><tr><td>0.20 <φ≤0.30</td><td>4</td></tr><tr><td>0.30 <φ≤0.50</td><td>1</td></tr><tr><td>0.50 < φ</td><td>None</td></tr></table></td></tr>
<tr><td>12</td><td>Dent on Polarizer
(Minor defect)</td><td><table><tr><td>Diameter</td><td>Acc. Qty</td></tr><tr><td>φ≤0.20</td><td>Ignore</td></tr><tr><td>0.20 <φ≤0.30</td><td>4</td></tr><tr><td>0.30 <φ≤0.50</td><td>1</td></tr><tr><td>0.50 < φ</td><td>None</td></tr></table></td></tr>
<tr><td>13</td><td>Bezel</td><td>13.1 No rust, distortion on the Bezel.
13.2 No visible fingerprints, stains or other contamination.</td></tr>
<tr><td>14</td><td>PCB</td><td>14.1 No distortion or contamination on PCB terminals.
14.2 All components on PCB must same as documented on the BOM/component layout.
14.3 Follow IPC-A-600F.</td></tr>
<tr><td>15</td><td>Soldering</td><td>Follow IPC-A-610C standard</td></tr>
<tr><td>16</td><td>Electrical Defect
(Major defect)</td><td>The below defects must be rejected.
16.1 Missing vertical / horizontal segment,
16.2 Abnormal Display.
16.3 No function or no display.
16.4 Current exceeds product specifications.
16.5 LCD viewing angle defect.
16.6 No Backlight.
16.7 Dark Backlight.
16.8 Touch Panel no function.</td></tr>
</table>

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

9.7. Classification of Defects

9.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

9.7.2. Two minor defects are equal to one major in lot sampling inspection.

9.8. Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

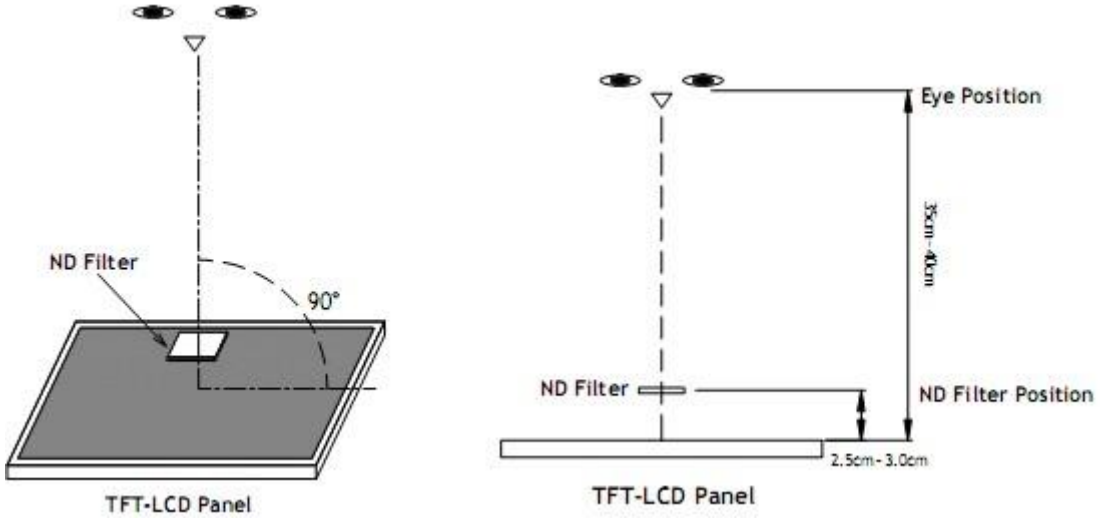
9.9. Packaging

9.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

9.9.2. Modules inside package box should have compliant mark.

9.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

10. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	**70℃, 96Hrs**	2	GB/T2423.2 -2008
2	Low Temperature Operating	**-20℃, 96Hrs**	2	GB/T2423.1 -2008
3	High Humidity Storage	**50℃, 90%RH, 96Hrs**	2	GB/T2423.3 -2016
4	High Temperature Storage	**80℃, 96Hrs**	2	GB/T2423.2 -2008
5	Low Temperature Storage	**-30℃, 96Hrs**	2	GB/T2423.1 -2008
6	Thermal Cycling Test	**-20℃, 60min~70℃, 60min, 20 cycles.**	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	-	GB/T5170.14 -2009
8	Electrical Static Discharge	Air:±4KV 150pF/330Ω 5 times	2	GB/T17626.2 -2018
		Contact:±2KV 150pF/330Ω 5 times		
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	-	GB/T2423.7 -2018

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value.

11. Precautions and Warranty

11.1. Safety

11.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

11.2. Handling

11.2.1. Reverse and use within ratings in order to keep performance and prevent damage.

11.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

11.3. Storage

11.3.1. Do not store the LCD module beyond the specified temperature ranges.

11.3.2. Strong light exposure causes degradation of polarizer and color filter.

11.4. Metal Pin (Apply to Products with Metal Pins)

11.4.1. Pins of LCD and Backlight

11.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

11.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

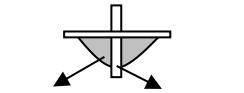
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

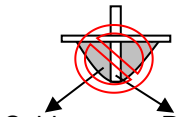
Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

11.4.1.3. Solder Wetting



Recommended



Not Recommended

11.4.2. Pins of EL

11.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

11.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

11.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270～290℃

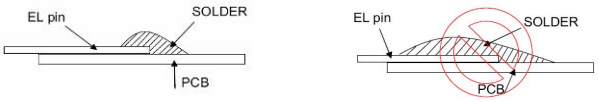
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

11.4.2.4. No horizontal press on the EL leads during soldering.

11.4.2.5. 180° bend EL leads three times is not allowed.

11.4.2.6. Solder Wetting

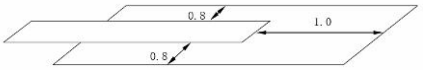


Recommended Not Recommended

11.4.2.7. The type of the solder iron:

Recommended Not Recommended

11.4.2.8. Solder Pad



11.5. Operation

11.5.1. Do not drive LCD with DC voltage

11.5.2. Response time will increase below lower temperature

11.5.3. Display may change color with different temperature

11.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

11.5.5. Do not connect or disconnect the LCM to or from the system when power is on.

11.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.

11.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

11.5.8. Do not display the fixed pattern for long time（we suggest the time not longer than one hour） because it may develop image sticking due to the TFT structure.

11.6. Static Electricity

11.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

11.6.2. The normal static prevention measures should be observed for work clothes and benches.

11.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

11.7. Limited Warranty

11.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.

11.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used

11.7.3. . After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

12. Packaging

TBD

13. Outline Drawing