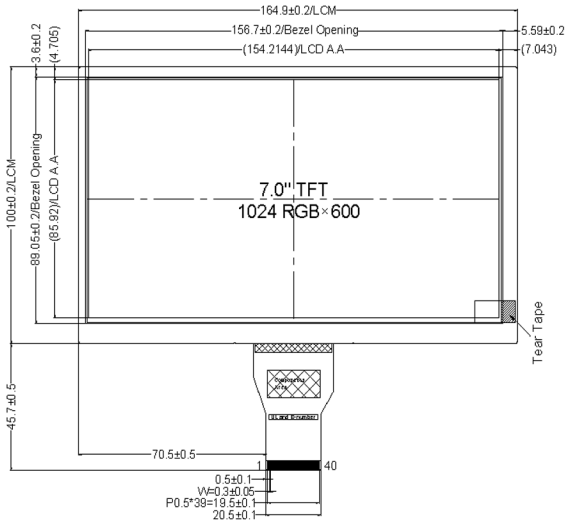
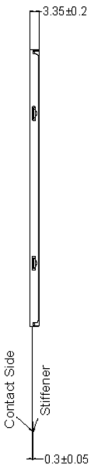
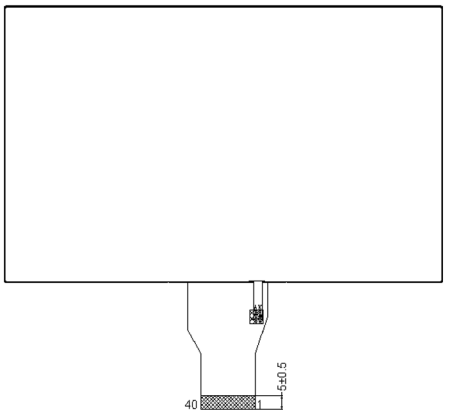
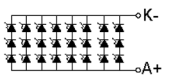








Backlight LED Circuit:3×8=24(LED)
If=160mA;Vf=8.1~9.6V

LCM PIN

Pin	Symbol
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	DIMO
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	NC
37	NC
38	VGH
39	LED+
40	LED+

NOTES:
1. Display size:7"TFT IPS
2. Viewing direction:full view
3. Display mode:Transmissive/Normal Black/Anti-glare
4. Operation temperature:-20°C~+70°C
5. Storage temperature:-30°C~+80°C
6. Driver IC:JD9165BA
7. Power supply voltage:3.3V
8. Backlight :White(24LED)/8.9(TYP)V/160mA
9. LCM Luminance:400(TYP)cd/㎡
10. Interface type:LVDS
11. RoHS must be complied
* The dimension with mark brackets "()" just for reference

General Tolerance±				
DIM \ Level	1	2	3	4
0-5	0.05	0.1	0.1	0.2
5-10	0.05	0.1	0.1	0.2
10-50	0.05	0.1	0.2	0.3
50-100	0.1	0.2	0.3	0.5
100-150	0.1	0.2	0.5	0.8
Level Seclect				◎

Compliance: RohS III (2015/863/EU)

Tolerances:			Date	Name
			12/24	dr
Modifications	Date	Name		

YDP LCD I 700 AG LVDS

knitter-switch

30 56 12

Page 1/25

PRODUCT SPECIFICATION

7.0” TFT LCD MODULE
MODEL: YDP LCD I 700 AG LVDS Ver:1.1

ROHS

< ◇> Preliminary Specification

< ◆> Finally Specification

CUSTOMER’S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY
-	TFT S. G. H 2024080	TFT 周福云 2024080	TFT L. L 2024080

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2024.04.25	LL	Initial Release	
1.1	2024.08.06	LL	Modify IC Modify DC Characteristics Modify Outline Drawing(B)	P4 P5 P25

Table of Contents

1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	7.0"	
LCD type	IPS TFT	
Display Mode	Transmissive/Normally black	
Resolution	1024 RGB x 600	Pixels
View Direction	FULL VIEW	Best Image
Module Outline	164.9(H) x 100(V) x 3.35(T) (Note1)	mm
Active Area	154.2144(H) x 85.92(V)	mm
Pixel Pitch	150.6 (H) x 143.2(V)	um
Pixel Arrangement	RGB-Stripe	
Polarizer Surface Treatment	Anti-glare	
Display Colors	262K/16.7 M	
Interface	6/8 bit LVDS Interface	
Driver IC	JD9165BA	-
With or Without Touch Panel	Without	
Operating Temperature	**-20～70**	ºC
Storage Temperature	**-30～80**	ºC
Weight	TBD	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25ºC

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.3	3.96	V
Storage temperature	T_{STG}	**-30**	**+80**	ºC
Operating temperature	T_{OP}	**-20**	**+70**	ºC

Note 1: If Ta below 50ºC, the maximal humidity is 90%RH, if Ta over 50ºC, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10℃, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	2.8	3.3	3.6	V
	AVDD	9	-	12	V
	VGH	15	18	20	V
	VGL	-12	-8	-6	V
	VCOM	2.7	3.8	5.4	V
Differential input high threshold voltage	RxVTH	-	-	37	mV
Differential input low threshold voltage	RxVTL	-37	-	-	mV
Input voltage range (singled-end)	RxVIN	400	-	1650	mV
Differential input voltage	|VID|	100	-	400	mV

Note : Typical VCOM is only a reference value. It must be optimized according to each LCM. Please use VR.

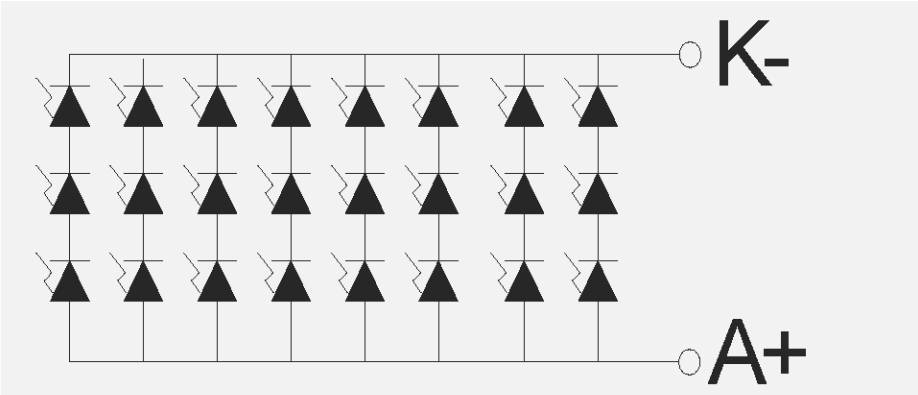
5. Backlight Characteristic

5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_F	Ta=25 °C, I_F=20mA/LED	8.1	**8.9**	9.6	V
Forward Current	I_F	Ta=25 °C, V_F=3.0V/LED	-	**160**	-	mA
Power dissipation	P_D		-	**1424**	-	mW
Uniformity	Avg		-	80	-	%
LED working life(25℃)	-		-	30,000	-	Hrs
Drive method	**Constant current**					
LED Configuration	24 White LEDs (3 LEDs in string and 8 groups in parallel)					

Note1：LED life time defined as follows: The final brightness is at 50% of original brightness.
The environmental conducted under ambient air flow, at Ta=25±2 °C,60%RH±5%, I_F=20mA/LED.

5.2. Backlighting circuit



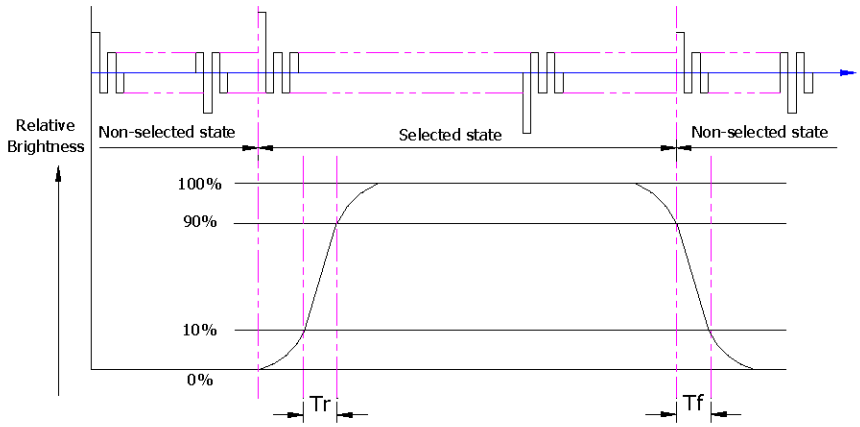
6. Optical Characteristics

Ta=25ºC, VDD=3.3V

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Luminance on TFT(I_f =20mA/LED)		Lv		**320**	**400**	**-**	cd/m²
	Contrast ratio(See 6.3)		CR		700	1000	-	
	Response time (See 6.2)		TR+TF		-	TBD	-	ms
	Chromaticity Transmissive (See 6.5)	Red	XR		-	**TBD**	-	
			YR		-	**TBD**	-	
		Green	XG		-	**TBD**	-	
			YG		-	**TBD**	-	
		Blue	XB		-	**TBD**	-	
			YB		-	**TBD**	-	
		White	XW		-	**TBD**	-	
			YW		-	**TBD**	-	
	Viewing Angle (See 6.4)	Horizontal	θX+	Center CR≥10	**70**	**80**	**-**	Deg.
			θX-		**70**	**80**	**-**	
		Vertical	φY+		**70**	**80**	**-**	
			φY-		**70**	**80**	**-**	
	NTSC Ratio(Gamut)				45	50	-	%

6.1. Definition of Response Time

6.1.1. Normally Black Type (Negative)

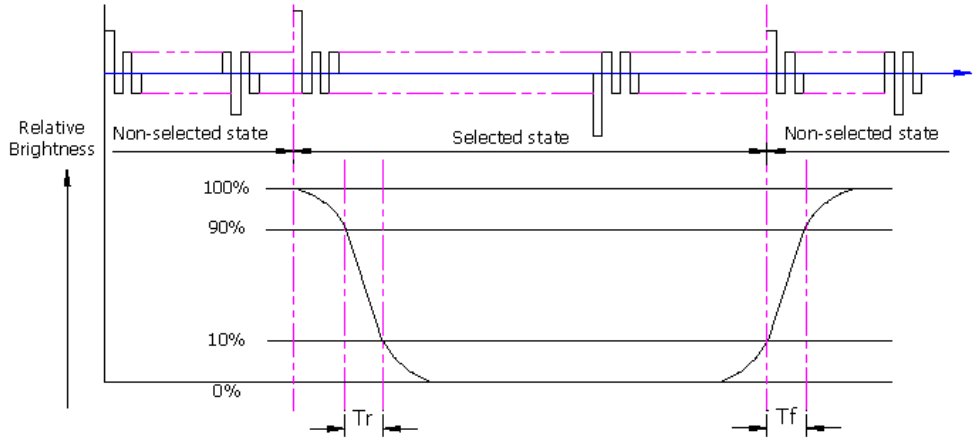


Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

6.1.2. Normally White Type (Positive)



Tr is the time it takes to change form non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

6.2. Definition of Contrast Ratio

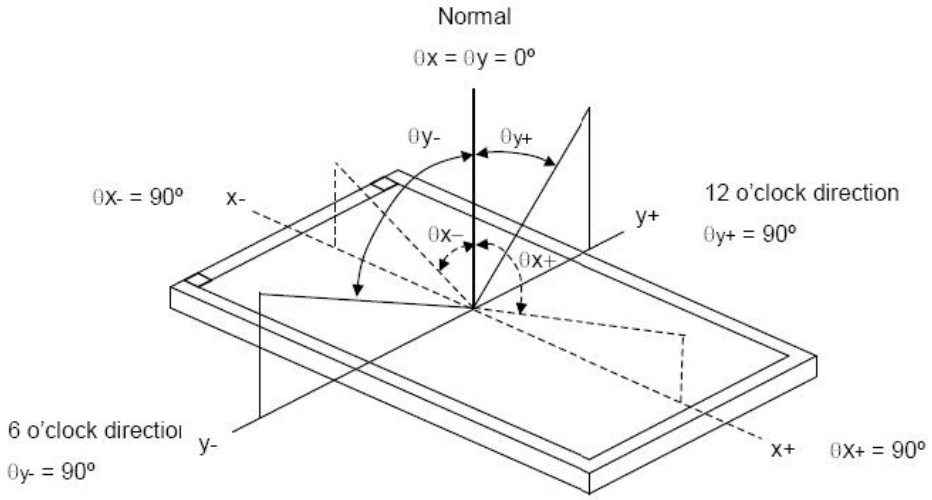
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.3. Definition of Viewing Angles



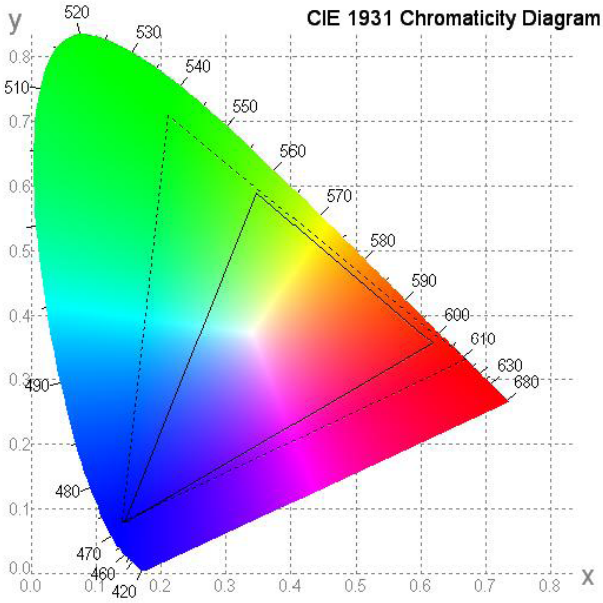
Measuring machine: LCD-5100 or EQUI

6.4. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.5. Definition of Surface Luminance, Uniformity and Transmittance

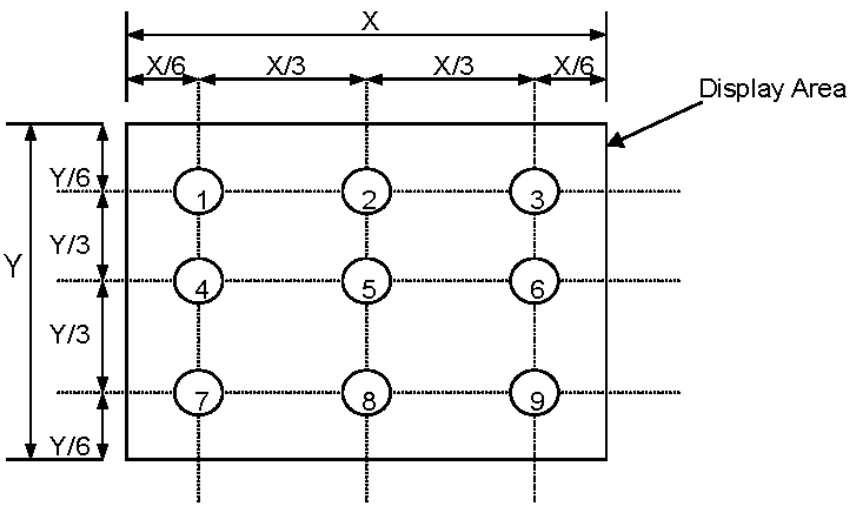
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.5.1. Surface Luminance: L_V = average (L_{P1}:L_{P9})

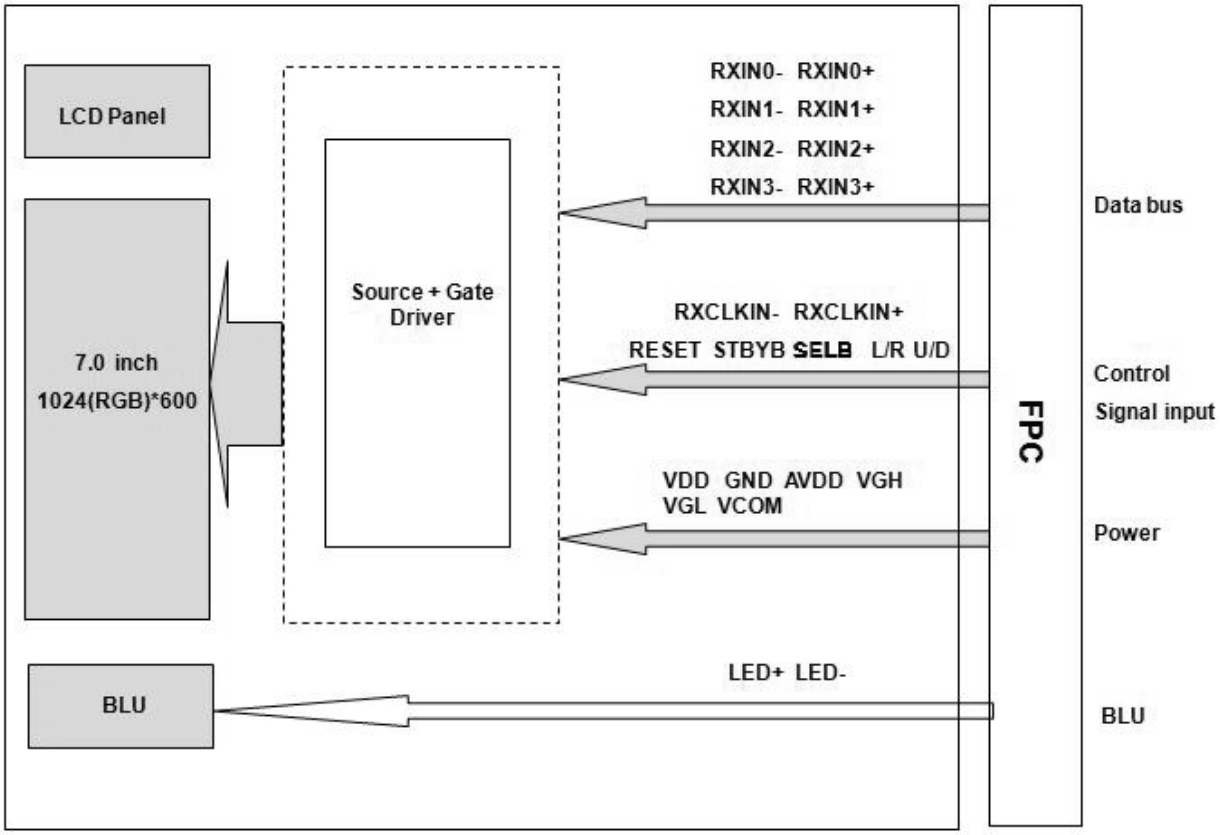
6.5.2. Uniformity = Minimal (L_{P1}:L_{P9}) / Maximal (L_{P1}:L_{P9}) * 100%

6.5.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

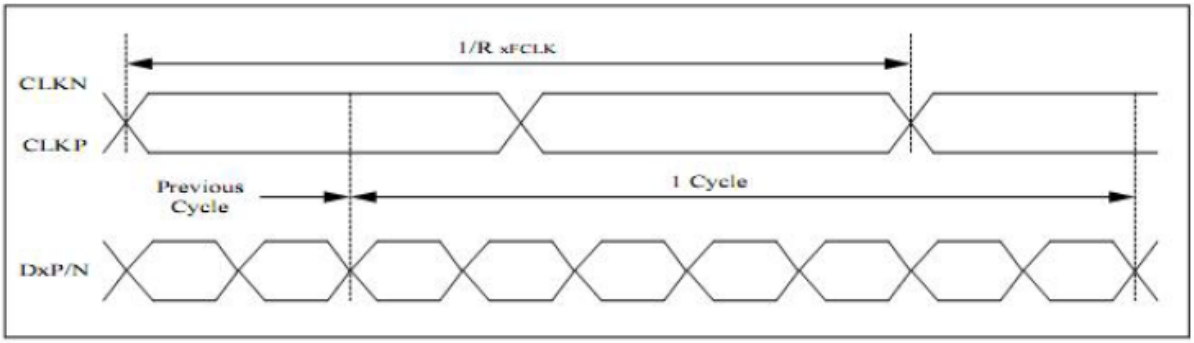
No.	Symbol	Function	Remark
1	VCOM	Common Voltage.	
2	VDD	Power Supply	
3	VDD	Power Supply	
4	NC	Not connected	
5	RESET	Global reset pin	
6	STBYB	Standby mode, Normally pulled high. STBYB="1", normal operation STBYB="0", timing controller, source driver will turn off	
7	GND	Ground	
8	RXIN0-	-LVDS differential data input.	
9	RXIN0+	+LVDS differential data input.	
10	GND	Ground	
11	RXIN1-	-LVDS differential data input.	
12	RXIN1+	+LVDS differential data input.	
13	GND	Ground	
14	RXIN2-	-LVDS differential data input.	
15	RXIN2+	+LVDS differential data input.	
16	GND	Ground	
17	RXCLKIN-	-LVDS differential clock input.	
18	RXCLKIN+	+LVDS differential clock input.	
19	GND	Ground	
20	RXIN3-	-LVDS differential data input.	
21	RXIN3+	+LVDS differential data input.	
22	GND	Ground	
23	NC	Not connected	
24	NC	Not connected	
25	GND	Ground	
26	NC	Not connected	
27	DIMO	Backlight CABC controller signal output	
28	SELB	"L": 8bit LVDS interface "H":6bit LVDS interface	
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	LED-	LED Cathode.	
32	LED-	LED Cathode.	
34	L/R	Horizontal scan direction. (Default pull Low) SHLR: 1 → S1537→S1536→.....S1→S0 SHLR: 0 → S0→S1→.....S1536→S1537	

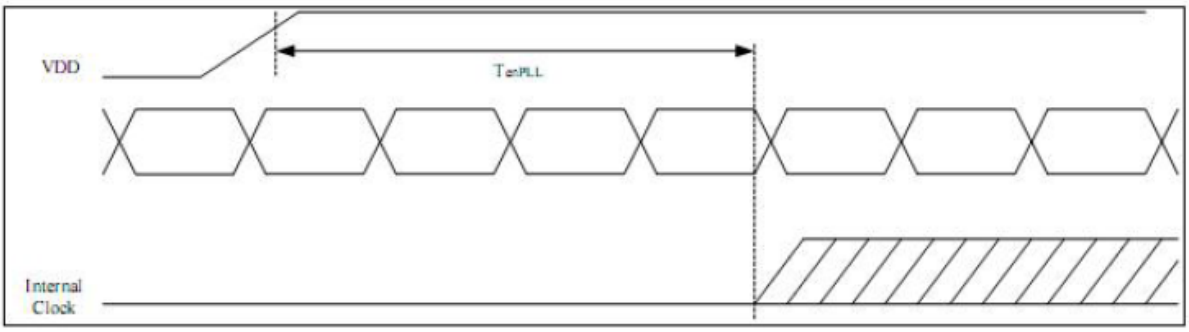
<table>
<tr><td>35</td><td>U/D</td><td>Vertical scan direction. (Default pull Low)
<table><tr><th>UPDN</th><th>Function</th></tr><tr><td>1</td><td>Bottom → Top</td></tr><tr><td>0</td><td>Top → Bottom</td></tr></table></td><td></td></tr>
<tr><td>35</td><td>VGL</td><td>Gate OFF Voltage.</td><td></td></tr>
<tr><td>36</td><td>NC</td><td>Not connected</td><td></td></tr>
<tr><td>37</td><td>NC</td><td>Not connected</td><td></td></tr>
<tr><td>38</td><td>VGH</td><td>Gate ON Voltage.</td><td></td></tr>
<tr><td>39</td><td>LED+</td><td>LED Anode.</td><td></td></tr>
<tr><td>40</td><td>LED+</td><td>LED Anode.</td><td></td></tr>
</table>

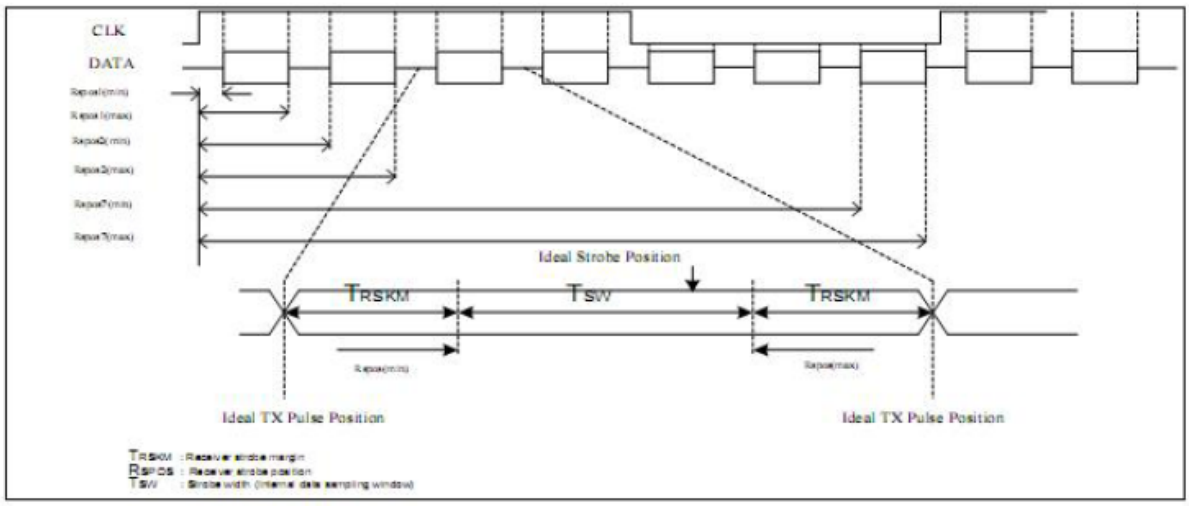
9. AC characteristics

9.1. Timing

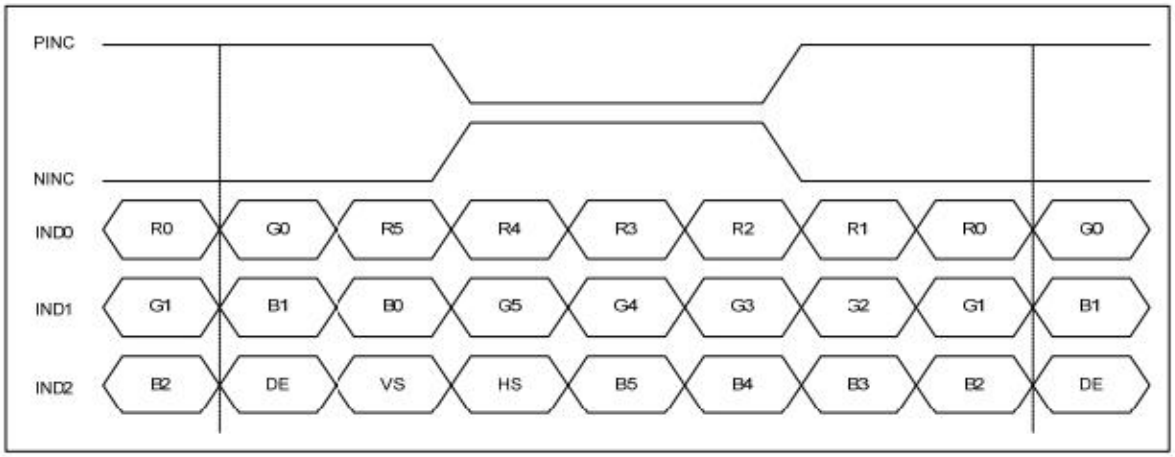
Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
Clock frequency	R_{xFCLK}	20	-	71	MHz	Refer to input timing table for each display resolution
Input data skew margin	T_{RSKM}	-0.2	-	0.2	UI	|VID| = 200mV RxVCM = 1.2V 1UI=1/(RxFCLKx7)
Clock high time	T_{LVCH}	-	3.5/(7* R_{xFCLK})	-	ns	
Clock low time	T_{LVCL}	-	3.5/(7* R_{xFCLK})	-	ns	
PLL wake-up time	T_{enPLL}	-	-	150	us	



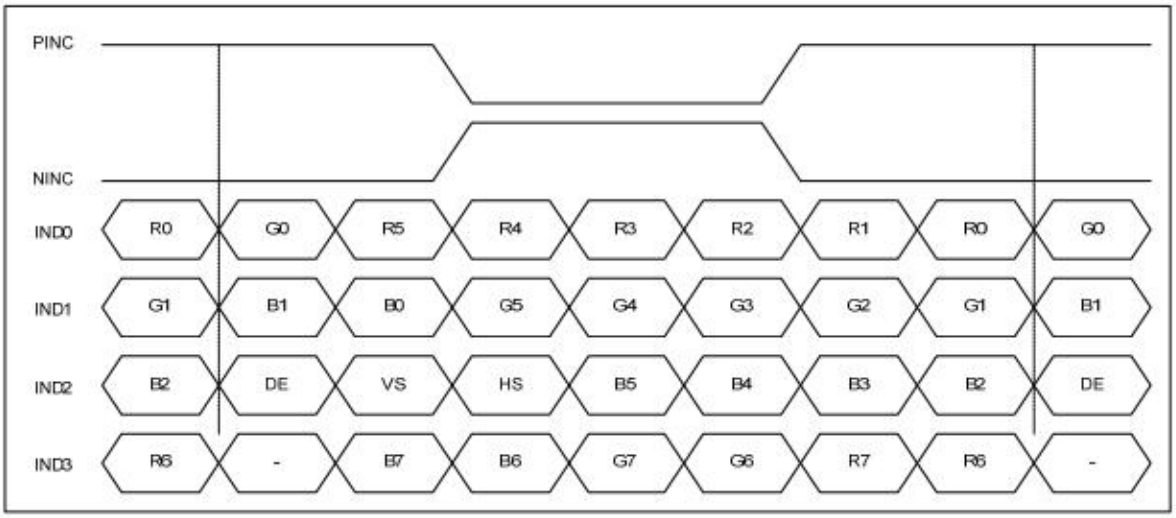




9.2. Data Input Format for LVDS



6 bit LVDS



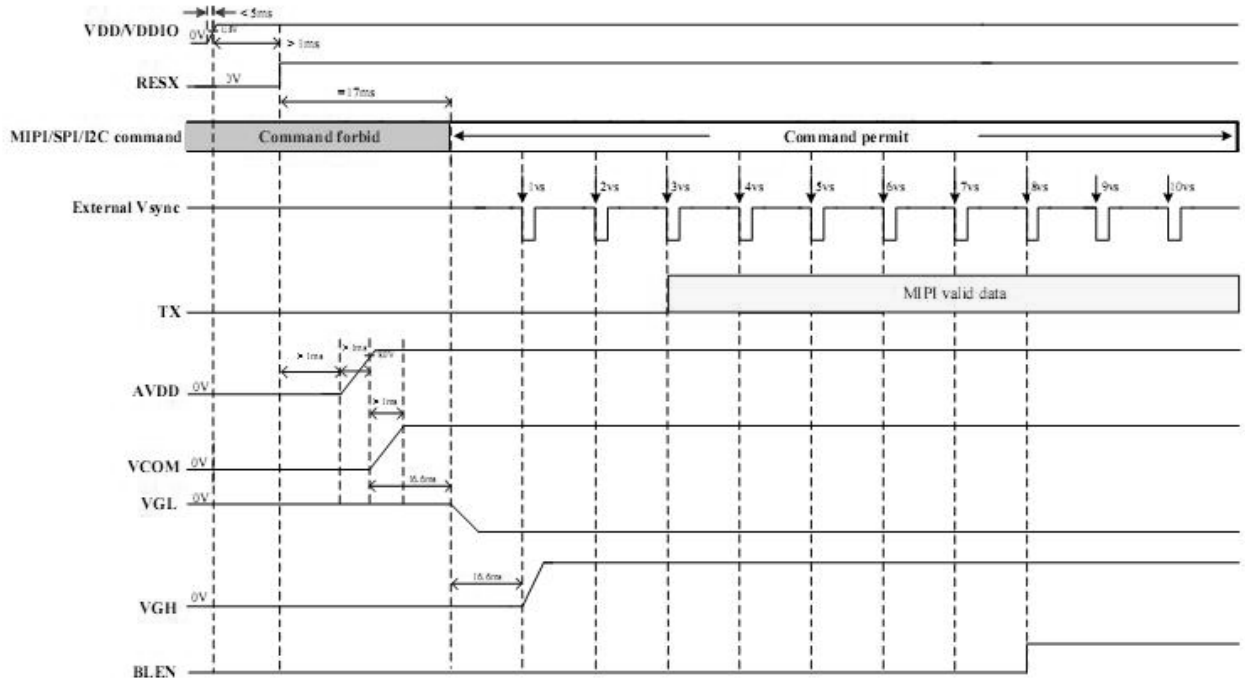
8 bit LVDS

9.3. LVDS timing characteristic

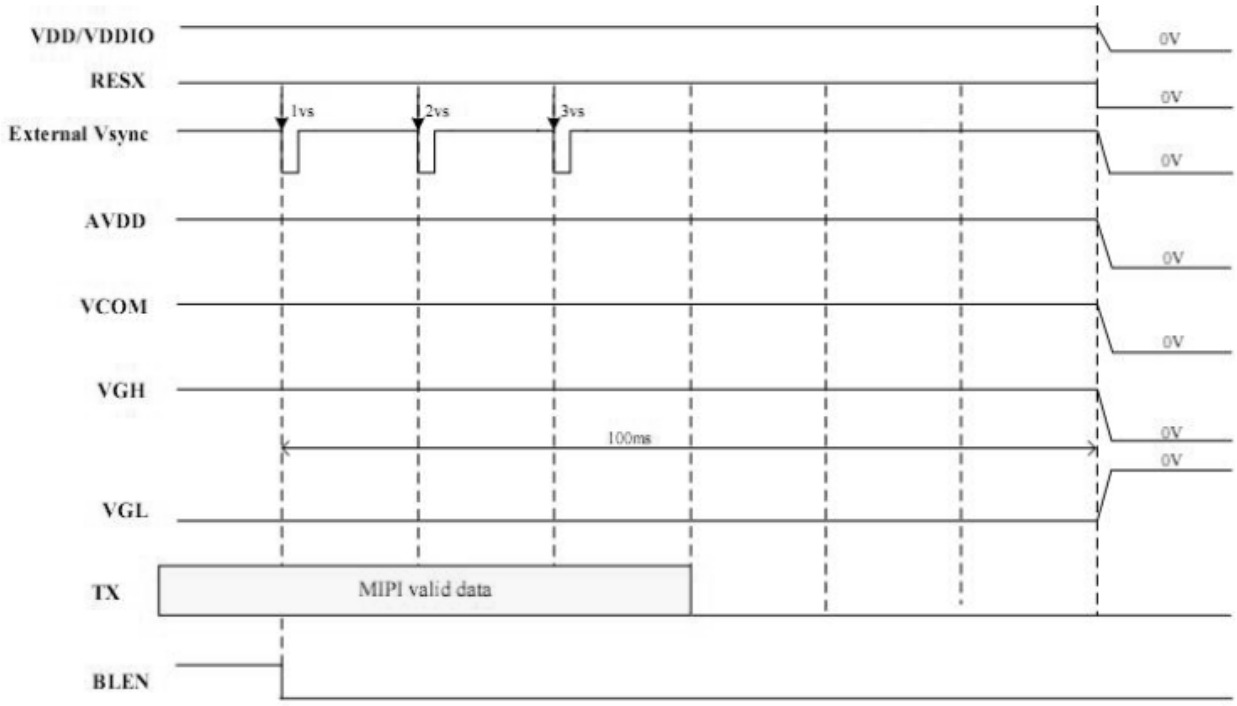
1024RGB×600				
LVDS Input Timing	**Symbol**	**Min**	**Typ**	**Max**
DCLK Frequency	-	41.4	51.2	67.2
Horizontal Total	tht	1114	1344	1400
Hsync Pulse width	ths	1	24	HBP-1
Horizontal Back Porch	thb	60	160	160
Horizontal Valid Data	thd		1024	
Horizontal Front Porch	thfp	30	160	216
Vertical Total	tvt	620	635	800
Vsync Pulse Width	tvs	1	2	VBP-1
Vertical Back Porch	tvb	8	23	100
Vertical Valid Data	tvd		600	
Vertical Front Porch	tvfp	12	12	100

10. Power On/Off Sequence

1) Power On timing chart



2) Power Off timing chart



11. Quality Assurance

11.1. Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

11.2. Standard for Quality Test

11.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

11.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

11.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

11.3. Nonconforming Analysis & Disposition

11.3.1. Nonconforming analysis:

11.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

11.3.2. Disposition of nonconforming:

11.3.2.1. Non-conforming product over PPM level will be replaced.

11.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

11.4. Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1. There is any discrepancy in standard of quality assurance.

11.4.2. Additional requirement to be added in product specification.

11.4.3. Any other special problem.

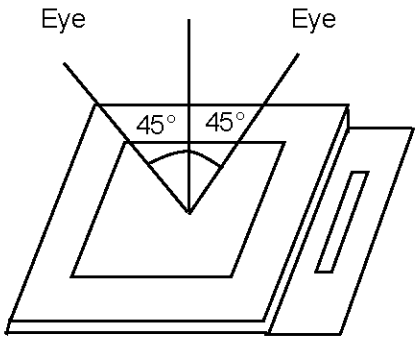
11.5. Standard of the Product Visual Inspection

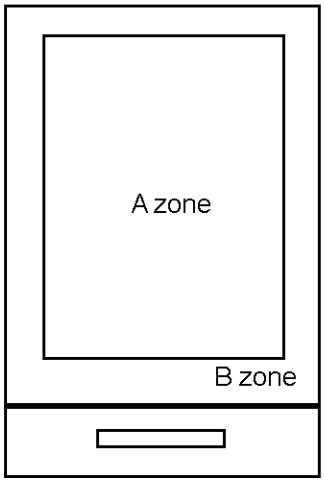
11.5.1. Appearance inspection:

11.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,



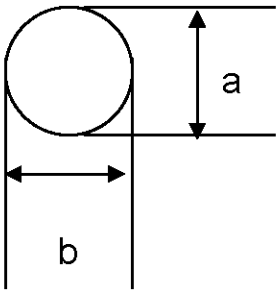


11.5.2. Basic principle:

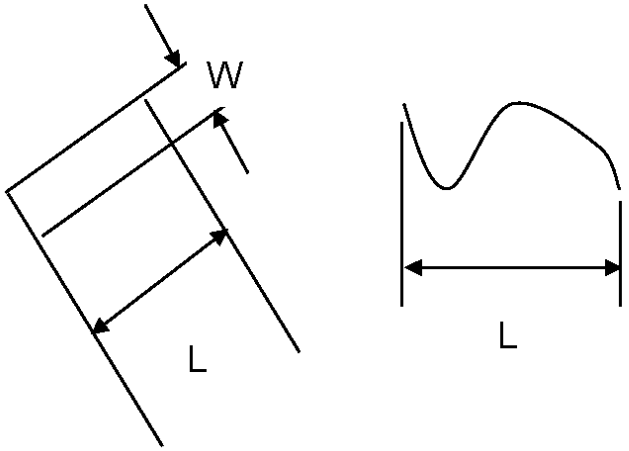
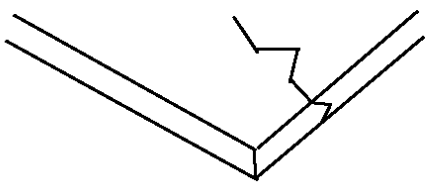
11.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

11.5.2.2. New item must be added on time when it is necessary.

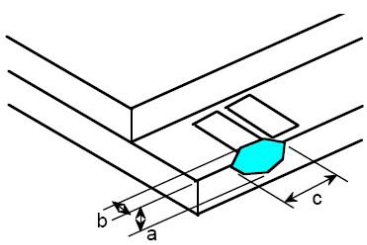
11.6. Inspection Specification for the TFT module

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>01</td><td>Black / White spot
Foreign material
(Round type)
Pinholes
Stain
Particles inside cell. (Minor defect)</td><td>


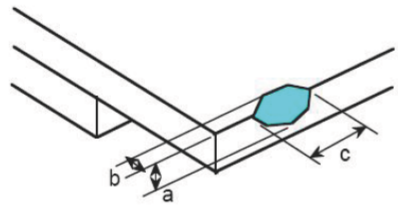
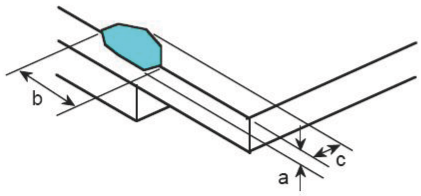
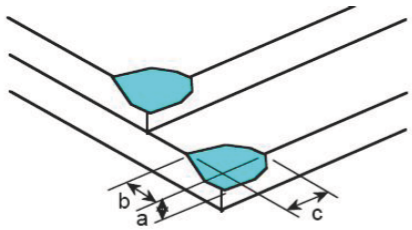
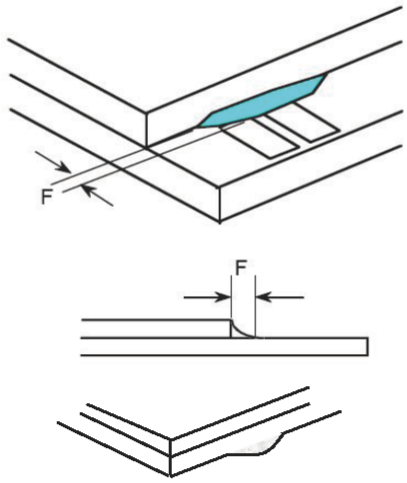
<table>
<tr><th>Area / Size</th><th>Acc. Qty</th></tr>
<tr><td>φ≤0.20</td><td>Ignore</td></tr>
<tr><td>0.20<φ≤0.50</td><td>N≤3</td></tr>
<tr><td>0.50<φ</td><td>0</td></tr>
</table>
φ= (a + b) /2
Distance between 2 defects should more than 5mm apart.
</td></tr>
<tr><td>02</td><td>Electrical Defect
(Minor defect)</td><td>
<table>
<tr><td rowspan="2">Bright dot</td><td>Display Area</td><td>Total</td><td rowspan="4">Note1</td></tr>
<tr><td>N⩽2</td><td>N⩽2</td></tr>
<tr><td>Dark dot</td><td>N⩽4</td><td>N⩽4</td></tr>
<tr><td>Total dot</td><td>N⩽4</td><td>N⩽4</td></tr>
<tr><td>Mura</td><td colspan="2">Not visible through 5% ND filters.</td><td>Note 2</td></tr>
</table>
Remark:
1. Bright dot caused by scratch and foreign object accords to item 1.
</td></tr>
</table>

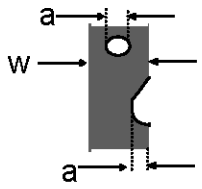
03	Black and White line Scratch Foreign material (Line type) (Minor defect)	 Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.
04	Glass Crack (Minor defect)	 Crack is potential to enlarge, any type is not allowed.

Length	Width	Acc. Qty
/	W ≦ 0.1	Ignore
L ≦ 2.5	0.1 ＜ W ≦ 0.2	3
L>2.5	0.2 ＜ W	0
Total		3

05	Glass Chipping Pad Area: (Minor defect) 	

Length and Width	Acc. Qty
c > 3.0, b< 1.0	1
c< 3.0, b< 1.0	3
a<Glass Thickness	

<table>
<tr>
<td>06</td>
<td>Glass Chipping Rear of Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>07</td>
<td>Glass Chipping Except Pad Area:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c > 3.0, b< 1.0</td><td>1</td></tr>
<tr><td>c< 3.0, b< 1.0</td><td>2</td></tr>
<tr><td>c< 3.0, b< 0.5</td><td>4</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>08</td>
<td>Glass Corner Chipping:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length and Width</th><th>Acc. Qty</th></tr>
<tr><td>c < 3.0, b< 3.0</td><td>Ignore</td></tr>
<tr><td colspan="2">a<Glass Thickness</td></tr>
</table></td>
</tr>
<tr>
<td>09</td>
<td>Glass Burr:
(Minor defect)
 
</td>
<td><table>
<tr><th>Length</th><th>Acc. Qty</th></tr>
<tr><td>F < 1.0</td><td>Ignore</td></tr>
</table>
Glass burr don’t affect assemble and module dimension.</td>
</tr>
</table>

<table>
<tr><td>10</td><td>FPC Defect:
(Minor defect)
 
</td><td>10.1 Dent, pinhole width a<w/3.
(w: circuitry width.)
10.2 Open circuit is unacceptable.
10.3 No oxidation, contamination and distortion.</td></tr>
<tr><td>11</td><td>Bubble on Polarizer
(Minor defect)</td><td><table><tr><td>Diameter</td><td>Acc. Qty</td></tr><tr><td>φ≤0.30</td><td>Ignore</td></tr><tr><td>0.30 <φ≤0.50</td><td>N≤2</td></tr><tr><td>0.50 < φ</td><td>N=0</td></tr></table></td></tr>
<tr><td>12</td><td>Dent on Polarizer
(Minor defect)</td><td><table><tr><td>Diameter</td><td>Acc. Qty</td></tr><tr><td>φ≤0.25</td><td>Ignore</td></tr><tr><td>0.25 <φ≤0.50</td><td>N≤4</td></tr><tr><td>0.50 < φ</td><td>None</td></tr></table></td></tr>
<tr><td>13</td><td>Bezel</td><td>13.1 No rust, distortion on the Bezel.
13.2 No visible fingerprints, stains or other contamination.</td></tr>
<tr><td>14</td><td>PCB</td><td>14.1 No distortion or contamination on PCB terminals.
14.2 All components on PCB must same as documented on the BOM/component layout.
14.3 Follow IPC-A-600F.</td></tr>
<tr><td>15</td><td>Soldering</td><td>Follow IPC-A-610C standard</td></tr>
<tr><td>16</td><td>Electrical Defect
(Major defect)</td><td>The below defects must be rejected.
16.1 Missing vertical / horizontal segment,
16.2 Abnormal Display.
16.3 No function or no display.
16.4 Current exceeds product specifications.
16.5 LCD viewing angle defect.
16.6 No Backlight.
16.7 Dark Backlight.
16.8 Touch Panel no function.</td></tr>
</table>

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

11.7. Classification of Defects

11.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

11.7.2. Two minor defects are equal to one major in lot sampling inspection.

11.8. Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

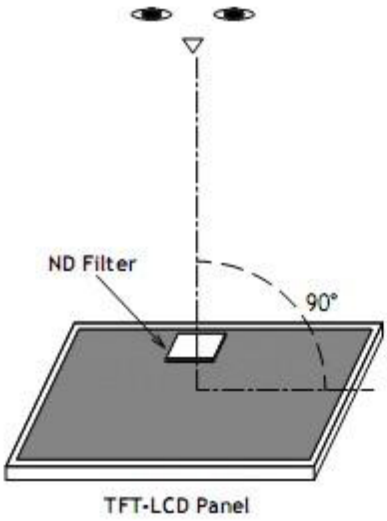
11.9. Packing

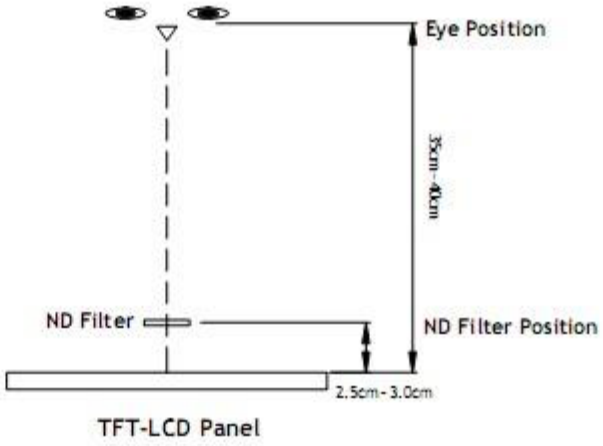
11.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

11.9.2. Modules inside package box should have compliant mark.

11.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.





Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm±50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	**70℃, 96Hrs**	2	GB/T2423.2 -2008
2	Low Temperature Operating	**-20℃, 96Hrs**	2	GB/T2423.1 -2008
3	High Humidity Storage	**50℃, 90%RH, 96Hrs**	2	GB/T2423.3 -2016
4	High Temperature Storage	**80℃, 96Hrs**	2	GB/T2423.2 -2008
5	Low Temperature Storage	**-30℃, 96Hrs**	2	GB/T2423.1 -2008
6	Thermal Cycling Test Storage	**-20℃, 60min ~ 70℃, 60min, 20 cycles.**	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	-	GB/T5170.14 -2009
8	Electrical Static Discharge	Air:±4KV 150pF/330Ω Contact:±2KV 150pF/330Ω	2	GB/T17626.2 -2018
9	**Drop Test (Packaged)**	Height:72cm(weight≤10kg),60cm（weight>10kg） 1 corner, 3 edges, 6 surfaces.	-	GB/T2423.7 -2018

Note1. No defection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

13. Precautions and Warranty

13.1. Safety

13.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

13.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

13.2. Handling

13.2.1. Reverse and use within ratings in order to keep performance and prevent damage.

13.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

13.3. Storage

13.3.1. Do not store the LCD module beyond the specified temperature ranges.

13.3.2. Strong light exposure causes degradation of polarizer and color filter

13.4. Metal Pin (Apply to Products with Metal Pins)

13.4.1. Pins of LCD and Backlight

13.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

13.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

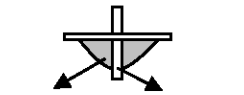
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

13.4.1.3. Solder Wetting



Recommended

Not Recommended

13.4.2. Pins of EL

13.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

13.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

13.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270～290℃

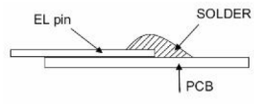
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

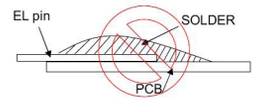
13.4.2.4. No horizontal press on the EL leads during soldering.

13.4.2.5. 180° bend EL leads three times is not allowed.

13.4.2.6. Solder Wetting

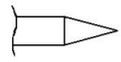


Recommended

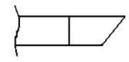


Not Recommended

13.4.2.7. The type of the solder iron:

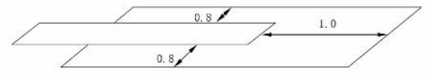


Recommended



Not Recommended

13.4.2.8. Solder Pad



13.5. Operation

13.5.1. Do not drive LCD with DC voltage

13.5.2. Response time will increase below lower temperature

13.5.3. Display may change color with different temperature

13.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

13.5.5. Do not connect or disconnect the LCM to or from the system when power is on.

13.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.

13.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

13.5.8. Do not display the fixed pattern for long time（we suggest the time not longer than one hour） because it may develop image sticking due to the TFT structure.

13.6. Static Electricity

13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

13.6.2. The normal static prevention measures should be observed for work clothes and benches.

13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

13.7. Limited Warranty

13.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.

13.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used

13.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

14. Packaging

TBD