# PRODUCT SPECIFICATION

## 1.29” OLED Display Module
## MODEL: YDP OLED R 129

ROHS

< ◇> Preliminary Specification

< ◆> Finally Specification

| CUSTOMER’S APPROVAL | |
|---|---|
| CUSTOMER : | |
| SIGNATURE: | DATE: |
| | |

| APPROVED BY | PM REVIEWED | PD REVIEWED | PREPARED BY |
|---|---|---|---|
| | | | |

knitter-switch

## Revision History

| Revision | Date | Originator | Detail | Remarks |
|---|---|---|---|---|
| 1.0 | 2019.07.02 | ZDT | Initial Release | |
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## Table of Contents

## 1. Module Parameter

| Features | Details | Unit |
|---|---|---|
| Display Size(Diagonal) | 1.29” | |
| Resolution | 128 x 96 | Pixels |
| Module Outline | 33 (H) x 25.8 (V) x 1.24 (T) (Note1 ) | mm |
| Active Area | 26.279(H) x 19.708(V) | mm |
| Pixel Size | 205 (H) x 205 (V) | um |
| Interface | 8/16/18-bit 6800-series Parallel Interface<br>8/16/18-bit 8080-series Parallel Interface<br>Serial Peripheral Interface | |
| With or without touch panel | Without | |
| Driver IC | SSD1351 | - |
| Weight | TBD | g |

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

## 2. Absolute Maximum Ratings

| Parameter | Symbol | Min | Max | Unit | Notes |
|---|---|---|---|---|---|
| Supply Voltage for Display | VCC | 8 | 21 | V | 1,2 |
| Supply Voltage | VCI | -0.3 | 4.0 | V | 1,2 |
| Operating Temperature | $T_{OP}$ | -40 | 70 | °C | 3 |
| Storage Temperature | $T_{STG}$ | -40 | 85 | °C | 3 |
| Life Time (90 cd/m$^2$) | | 11,000 | - | hour | 4 |
| Life Time (80 cd/m$^2$) | | 12,000 | - | hour | 4 |

Note 1: All the above voltages are on the basis of “$V_{SS}$ = 0V”.

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 4. “Optics & Electrical Characteristics”. If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

Note 3: The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80°C.

Note 4: $V_{CC}$ = 15V, $T_a$ = 25°C, 50% Checkerboard.

End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

## 3. Interface Pins Definition

| No. | Symbol | Function |
|---|---|---|
| 1 | NC | No connection. |
| 2 | VCC | Power supply for panel driving voltage. |
| 3 | VCOMH | COM signal deselected voltage level. A capacitor should be connected between this pin an VSS. |
| 4 | VDDIO | Power supply for interface logic level. |
| 5 | VSL | This is segment voltage reference pin. |
| 6 | D17 | These pins are bi-directional data bus connecting to the MCU data bus. |
| 7 | D16 | |
| 8 | D15 | |
| 9 | D14 | |
| 10 | D13 | |
| 11 | D12 | |
| 12 | D11 | |
| 13 | D10 | |
| 14 | D9 | |
| 15 | D8 | |
| 16 | D7 | |
| 17 | D6 | |
| 18 | D5 | |
| 19 | D4 | |
| 20 | D3 | |
| 21 | D2 | |
| 22 | D1 | |
| 23 | D0 | |
| 24 | E | 8080: data read enable pin; 6800:Read/Write enable pin. |
| 25 | R/W# | 8080: data write enable pin; 6800:Read/Write select pin. |
| 26 | BS0 | Interface select pin. |
| 27 | BS1 | |
| 28 | NC | No connection. |
| 29 | CS# | Chip select pin. |
| 30 | D/C# | H: Data, L: Command. |
| 31 | RES# | Hardware Reset pin (Low active). |
| 32 | IREF | A resistor should be connected between this pin and VSS. |
| 33 | VDD | Power supply pin for core logic operation. |
| 34 | NC | No connection. |
| 35 | NC | No connection. |
| 36 | VCI | Digital voltage power supply. |
| 37 | NC | No connection. |
| 38 | VSS | Ground. |
| 39 | NC | No connection. |

# 4. Optics & Electrical Characteristics

## 4.1. Optics Characteristics

| Characteristics | Symbol | Conditions | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Pixel Luminance | Lbr | | 70 | 90 | - | cd/m2 |
| Standby Luminance | Lbr | | - | 40 | - | cd/m2 |
| C.I.E. (White) | (x) | C.I.E. 1931 | 0.24 | 0.28 | 0.32 | |
| | (y) | | 0.28 | 0.32 | 0.36 | |
| C.I.E. (Red) | (x) | | 0.62 | 0.66 | 0.70 | |
| | (y) | | 0.29 | 0.33 | 0.37 | |
| C.I.E. (Green) | (x) | | 0.26 | 0.30 | 0.34 | |
| | (y) | | 0.59 | 0.63 | 0.67 | |
| C.I.E. (Blue) | (x) | | 0.10 | 0.14 | 0.18 | |
| | (y) | | 0.14 | 0.18 | 0.22 | |
| Dark Room Contrast | CR | | 2000:1 | - | - | |
| Viewing Angle | | | 160 | - | - | degree |
| Response Time | | | - | 10 | - | μs |

## 4.2. DC Characteristics

| Characteristics | Symbol | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| Analog power supply | VCC | 14.5 | 15 | 15.5 | V |
| Digital power supply | VCI | 2.4 | 2.7 | 3.5 | V |
| I/O voltage power supply | VDDIO | 1.65 | 1.8 | VCI | V |
| Operating Current for VDD | IDD | - | 170 | 190 | μA |
| Operating Current for VCC | ICC | - | 1.25 | 1.4 | mA |
| Operating Current for VDDIO | $I_{DDIO}$ | - | 0.5 | 10 | μA |
| Operating Current for VCI | $I_{CI}$ (External VDD = 2.5V) | - | 60 | 70 | μA |
| | $I_{CI}$(Internal VDD) | - | 260 | 290 | μA |
| High Level Input | VIH | 0.8×VDDIO | - | VDDIO | V |
| Low Level Input | VIL | 0 | - | 0.2×VDDIO | V |
| High Level Output | VOH | 0.9×VDDIO | - | VDDIO | V |
| Low Level Output | VOL | 0 | - | 0.1×VDDIO | V |

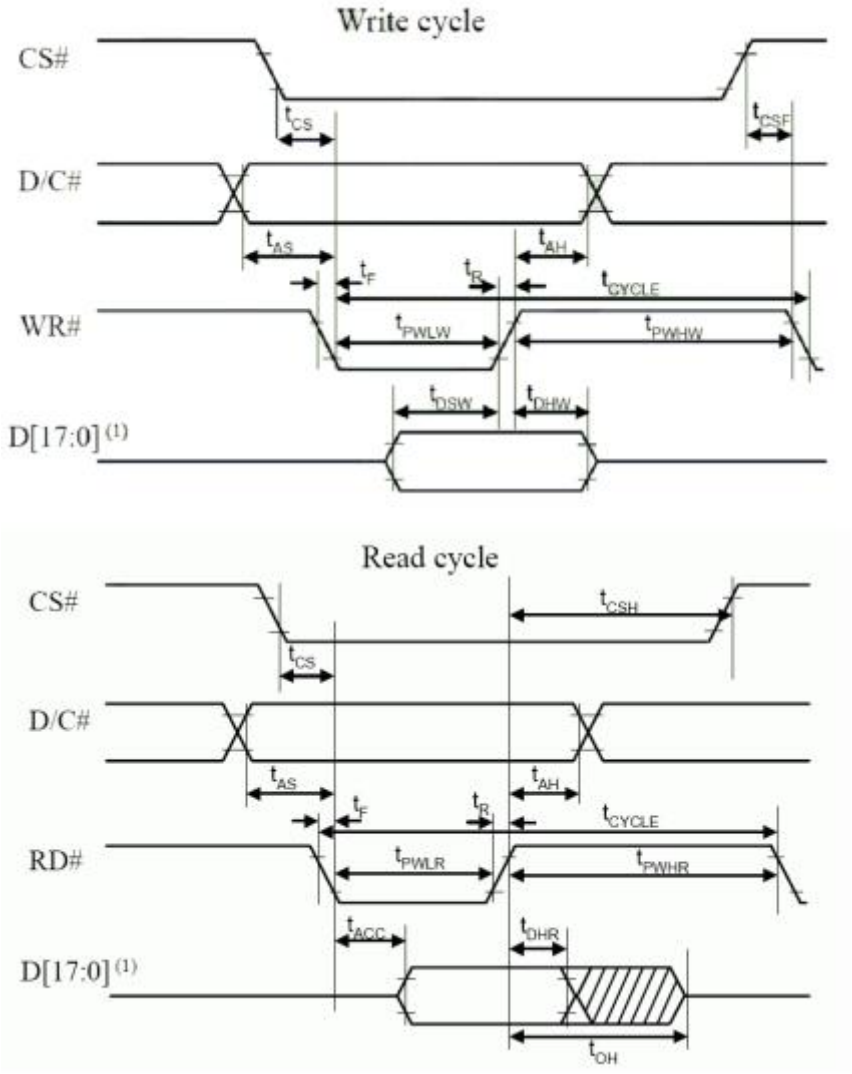
## 4.3. INTERFACE TIMING CHART

### 4.3.1. 8080-Series MCU Parallel Interface Timing Characteristics

($V_{DD}$ - $V_{SS}$ = 2.4 to 2.6V, $V_{DDIO}$=1.65V, $V_{CI}$ = 2.8V, $T_A$ = 25°C)

| Symbol | Parameter | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| $t_{CYCLE}$ | Clock Cycle Time | 300 | - | - | ns |
| $t_{AS}$ | Address Setup Time | 10 | - | - | ns |
| $t_{AH}$ | Address Hold Time | 0 | - | - | ns |
| $t_{DSW}$ | Write Data Setup Time | 40 | - | - | ns |
| $t_{DHW}$ | Write Data Hold Time | 7 | - | - | ns |
| $t_{DHR}$ | Read Data Hold Time | 20 | - | - | ns |
| $t_{OH}$ | Output Disable Time | - | - | 70 | ns |
| $t_{ACC}$ | Access Time | - | - | 140 | ns |
| $t_{PWLR}$ | Read Low Time | 150 | - | - | ns |
| $t_{PWLW}$ | Write Low Time | 60 | - | - | ns |
| $t_{PWHR}$ | Read High Time | 60 | - | - | ns |
| $t_{PWHW}$ | Write High Time | 60 | - | - | ns |
| $t_R$ | Rise Time | - | - | 15 | ns |
| $t_F$ | Fall Time | - | - | 15 | ns |
| $t_{CS}$ | Chip select setup time | 0 | - | - | ns |
| $t_{CSH}$ | Chip select hold time to read signal | 0 | - | - | ns |
| $t_{CSF}$ | Chip select hold time | 20 | - | - | ns |

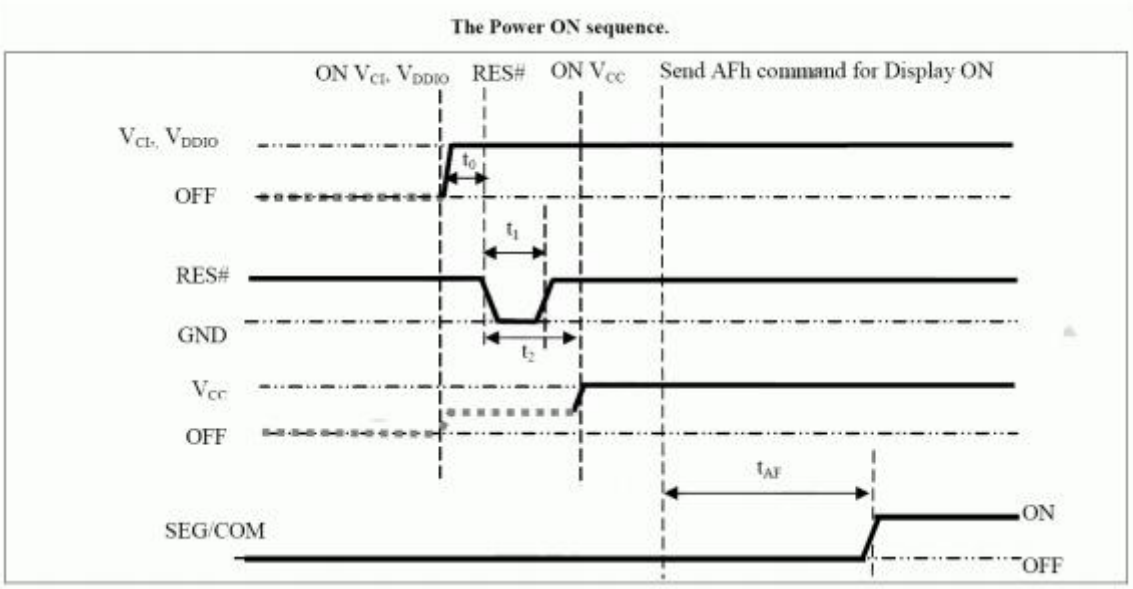
**8080-series MCU parallel interface characteristics**



### 4.3.2. POWER ON / OFF SEQUENCE

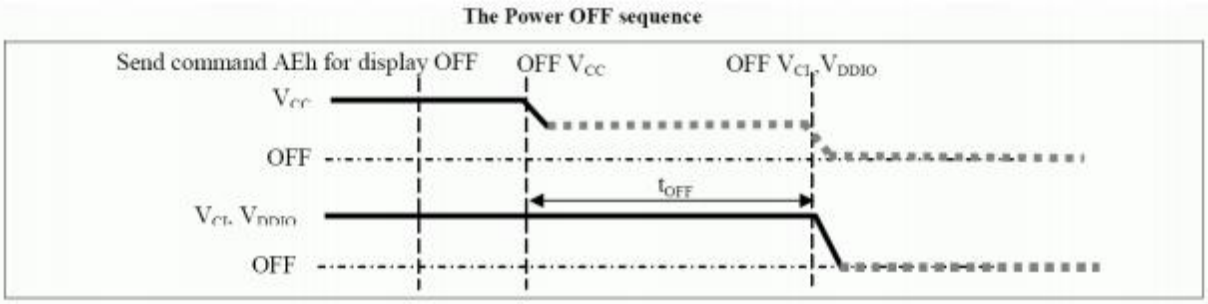
Power ON sequence:

1. Power ON VCI, VDDIO.
2. After VCI, VDDIO become stable, set wait time at least 1ms (t0) for internal VDD become stable. Then set RES# pin LOW (logic low) for at least 2us (t1) and then HIGH (logic high).
3. After set RES# pin LOW (logic low), wait for at least 2us (t2). Then Power ON VCC.
4. After VCC become stable, send command AFh for display ON. SEG/COM will be ON after 200ms(tAF).




Power OFF sequence:

1. Send command AEh for display OFF.
2. Power OFF VCC.
3. Wait for tOFF. Power OFF VCI, VDDIO.(where Minimum tOFF=80ms, Typical tOFF=100ms)




Note:

(1) Since an ESD protection circuit is connected between VCI, VDDIO and VCC, VCC becomes lower than VCI whenever VCI,VDDIO is ON and VCC is OFF as shown in the dotted line of VCC in above figures.

(2) VCC should be kept float (disable) when it is OFF.

(3) VCI, VDDIO should not be Power OFF before VCC Power OFF.

(4) The register values are reset after t1.

(5) Power pins (VDD, VCC) can never be pulled to ground under any circumstance.

# 5. Outgoing Quality Control Specifications

## 5.1. Environment Required

Customer's test & measurement are required to be conducted under the following conditions:

| | |
|---|---|
| Temperature: | 23 ± 5°C |
| Humidity: | 55 ± 15% RH |
| Fluorescent Lamp: | 30W |
| Distance between the Panel & Lamp: | ≥ 50cm |
| Distance between the Panel & Eyes of the Inspector: | ≥ 30cm |

Finger glove (or finger cover) must be worn by the inspector.

Inspection table or jig must be anti-electrostatic.

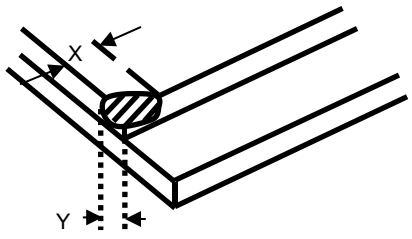
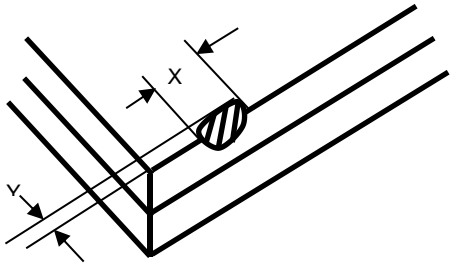
## 5.2. Sampling Plan

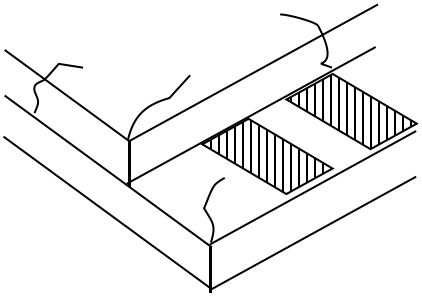
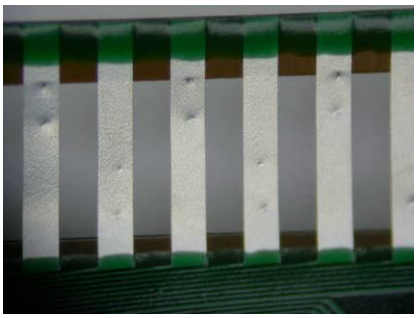
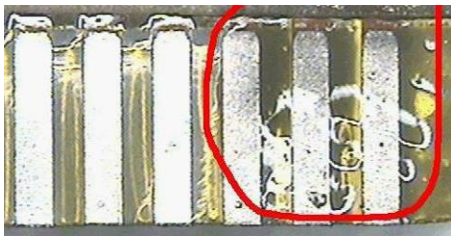
Level II, Normal Inspection, Single Sampling, MIL-STD-105E

## 5.3. Criteria & Acceptable Quality Level

| Partition | AQL | Definition |
|---|---|---|
| Major | 0.65 | Defects in Pattern Check (Display On) |
| Minor | 1.0 | Defects in Cosmetic Check (Display Off) |

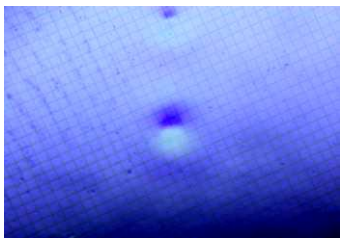
5.3.1. Cosmetic Check (Display Off) in Non-Active Area

| Check Item | Classification | Criteria |
|---|---|---|
| Panel General Chipping | Minor | X > 6 mm (Along with Edge)<br>Y > 1 mm (Perpendicular to edge)<br>  <br>  |

| | | |
|---|---|---|
| Panel Crack | Minor | Any crack is not allowable.<br>  |
| Copper Exposed<br>(Even Pin or Film) | Minor | Not Allowable by Naked Eye Inspection |
| Film or Trace Damage | Minor |  |
| Terminal Lead Prober Mark | Acceptable |  |
| Glue or Contamination on Pin<br>(Couldn't Be Removed by Alcohol) | Minor |  |
| Ink Marking on Back Side of panel<br>(Exclude on Film) | Acceptable | Ignore for Any |

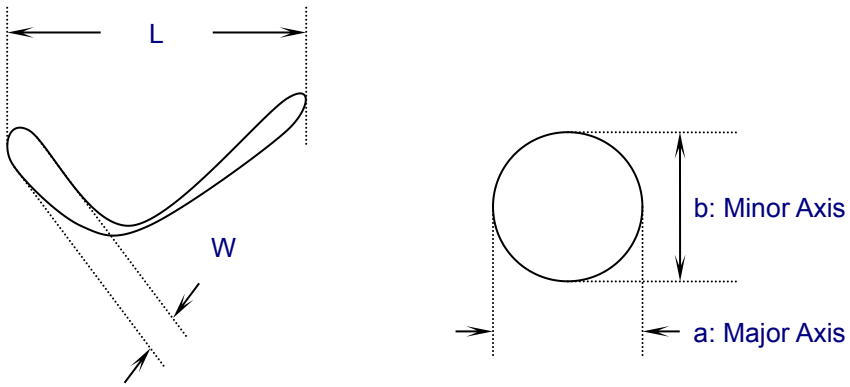
5.3.2. Cosmetic Check (Display Off) in Active Area

It is recommended to execute in clear room environment (class 10k) if actual in necessary.

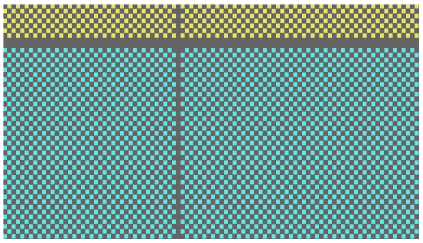
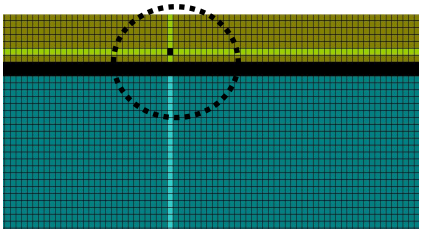
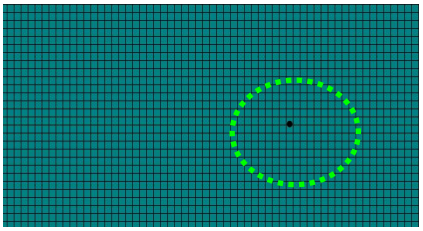
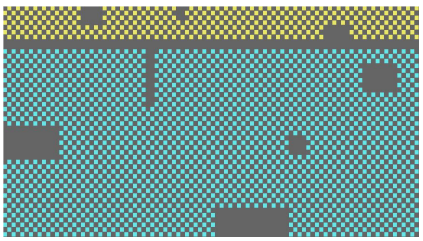
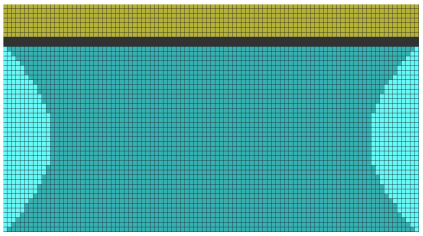
| Check Item | Classification | Criteria |
|---|---|---|
| Any Dirt & Scratch on Polarizer's Protective Film | Acceptable | Ignore for not Affect the Polarizer |
| Scratches, Fiber, Line-Shape Defect (On Polarizer) | Minor | W ≤ 0.1 Ignore<br>W > 0.1<br>L ≤ 2 n ≤ 1<br>L > 2 n = 0 |
| Dirt, Black Spot, Foreign Material, (On Polarizer) | Minor | Φ ≤ 0.1 Ignore<br>0.1 < Φ ≤ 0.25 n ≤ 1<br>0.25 < Φ n = 0 |
| Dent, Bubbles, White spot (Any Transparent Spot on Polarizer) | Minor | Φ ≤ 0.5<br>➔ Ignore if no Influence on Display<br>0.5 < Φ n = 0<br>  |
| Fingerprint, Flow Mark (On Polarizer) | Minor | Not Allowable |

Note 1：Protective film should not be tear off when cosmetic check.

Note 2：Definition of W & L & Φ (Unit: mm): Φ = (a + b) / 2



5.3.3. Pattern Check (Display On) in Active Area

| Check Item | Classification | Criteria |
|---|---|---|
| No Display | Major |  |
| Missing Line | Major |  |
| Pixel Short | Major |  |
| Darker Pixel | Major |  |
| Wrong Display | Major |  |
| Un-uniform | Major |  |

# 6. Reliability Specification

## 6.1. Contents of Reliability Tests

| No | Item | Condition | Quantity |
|---|---|---|---|
| 1 | High Temperature Operating | **70℃, 120Hrs** | 2 |
| 2 | Low Temperature Operating | **-40℃, 120Hrs** | 2 |
| 3 | High Humidity | **65℃, 90%RH, 96Hrs** | 2 |
| 4 | High Temperature Storage | **85℃, 240Hrs** | 2 |
| 5 | Thermal Cycling Test | **-40℃, 30min ~ 85℃, 30min, 20 cycles.** | 2 |
| 6 | Vibration | Frequency : 5~50HZ, 0.5G<br>Scan rate : 1 oct/min<br>Time : 2 hrs/axis<br>Test axis : X, Y, Z | 2 |
| 7 | Electrical Static Discharge | Air:±8KV, 10 times | 2 |
| 8 | Drop Test<br>(Packaged) | Height: 120cm<br>Sequence : 1 angle、3 edges and 6 faces<br>Cycles: 1 | 2 |

Note1. The samples used for the above tests do not include polarizer.

Note2. No moisture condensation is observed during tests.

## 6.2. Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5℃; 55±15% RH.

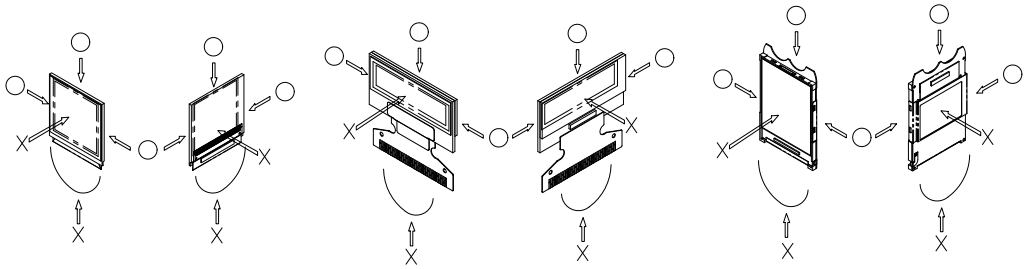
# 7. Precautions When Using These OLED Display Modules

## 7.1. Handling Precautions

1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
4) The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module.
5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
   * Scotch Mending Tape No. 810 or an equivalent

   Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.

   Also, pay attention that the following liquid and solvent may spoil the polarizer:
   * Water
   * Ketone
   * Aromatic Solvents
6) Hold OLED display module very carefully when placing OLED display module into the system housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



7) Do not apply stress to the driver IC and the surrounding molded sections.
8) Do not disassemble nor modify the OLED display module.
9) Do not apply input signals while the logic power is off.
10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
    * Be sure to make human body grounding when handling OLED display modules.
    * Be sure to ground tools to use or assembly such as soldering irons.
    * To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
    * Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
11) Protection film is being applied to the surface of the display panel and removes the

protection film before assembling it. At this time, if the OLED display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5).

12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

### 7.2. Storage Precautions

1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. and, also, avoiding high temperature and high humidity environment or low temperature (less than 0 ° C) environments. (We recommend you to store these modules in the packaged state when they were shipped from Newvision technology Co.,Ltd.)
   At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

### 7.3. Designing Precautions

1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the $V_{IL}$ and $V_{IH}$ specifications and, at the same time, to make the signal line cable as short as possible.
3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit ($V_{DD}$). (Recommend value: 0.5A)
4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
5) As for EMI, take necessary measures on the equipment side basically.
6) When fastening the OLED display module, fasten the external plastic housing section.
7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module.
8) The electric potential to be connected to the rear face of the IC chip should be as follows: SSD1316

   *Connection (contact) to any other potential than the above may lead to rupture of the IC.

### 7.4. Precautions when disposing of the OLED display modules

Request the qualified companies to handle industrial wastes when disposing of the OLED display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

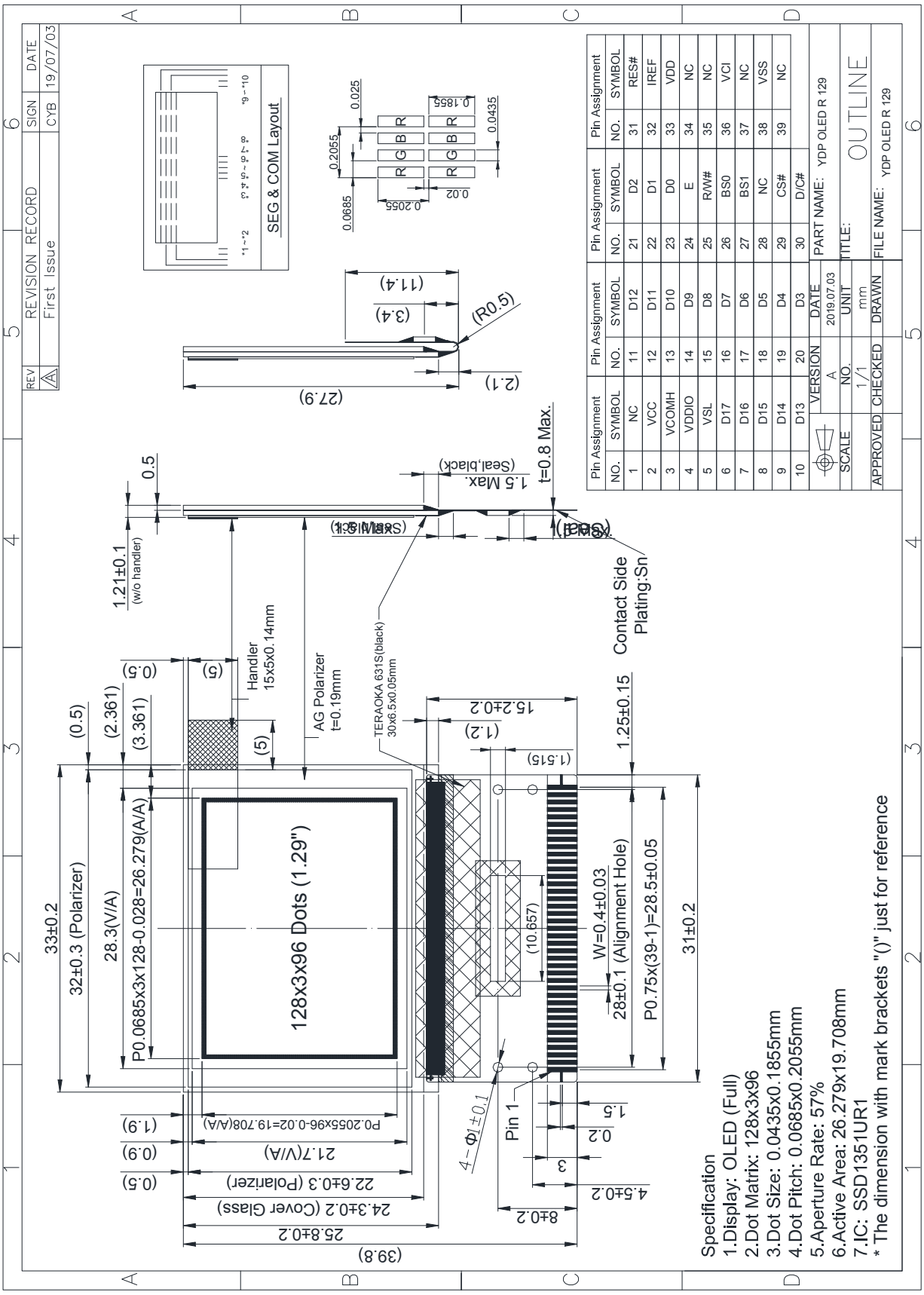
### 7.5. Other Precautions

1) When an OLED display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.
   Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module.
2) To protect OLED display modules from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the OLED display modules.
   * Pins and electrodes
   * Pattern layouts such as the FPC
3) With this OLED display module, the OLED driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this OLED driver is exposed to light, malfunctioning may occur.
   * Design the product and installation method so that the OLED driver may be shielded from light in actual usage.
   * Design the product and installation method so that the OLED driver may be shielded from light during the inspection processes.
4) Although this OLED display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. It therefore is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
5) We recommend you to construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise.

### 7.6. Warranty

The warranty period shall last twelve months from the date of delivery. Buyer shall be completed to assemble all the processes within the effective twelve months. We shall be liable for replacing any products which contain defective material or process which do not conform to the product specification, applicable drawings and specifications during the warranty period. All products must be preserved, handled and appearance to permit efficient handling during warranty period. The warranty coverage would be exclusive while the returned goods are out of the terms above.

## 8. Outline Drawing




| NO. | SYMBOL | NO. | SYMBOL | NO. | SYMBOL |
|---|---|---|---|---|---|
| 1 | NC | 11 | D12 | 21 | D2 |
| 2 | VCC | 12 | D11 | 22 | D1 |
| 3 | VCOMH | 13 | D10 | 23 | D0 |
| 4 | VDDIO | 14 | D9 | 24 | E |
| 5 | VSL | 15 | D8 | 25 | R/W# |
| 6 | D17 | 16 | D7 | 26 | BS0 |
| 7 | D16 | 17 | D6 | 27 | BS1 |
| 8 | D15 | 18 | D5 | 28 | NC |
| 9 | D14 | 19 | D4 | 29 | CS# |
| 10 | D13 | 20 | D3 | 30 | D/C# |

| NO. | SYMBOL |
|---|---|
| 31 | RES# |
| 32 | IREF |
| 33 | VDD |
| 34 | NC |
| 35 | NC |
| 36 | VCI |
| 37 | NC |
| 38 | VSS |
| 39 | NC |

| | |
|---|---|
| VERSION | A |
| DATE | 2019.07.03 |
| SCALE | 1/1 |
| UNIT | mm |
| APPROVED | |
| CHECKED | |
| DRAWN | |